


LCFC Confidential

Renoir E14/E15 GEN2 Rev0.2 Schematic

***AMD Renoir FP6 Processor with DDR4
NM-C771***

2019-09-17 Rev0.2

Security Classification	LC Future Center Secret Data			Title	
Issued Date	2019/06/21	Deciphered Date	2019/06/21	COVER PAGE	
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				Date: Friday, March 20, 2020	Rev 0.2
				Sheet 1	of 50



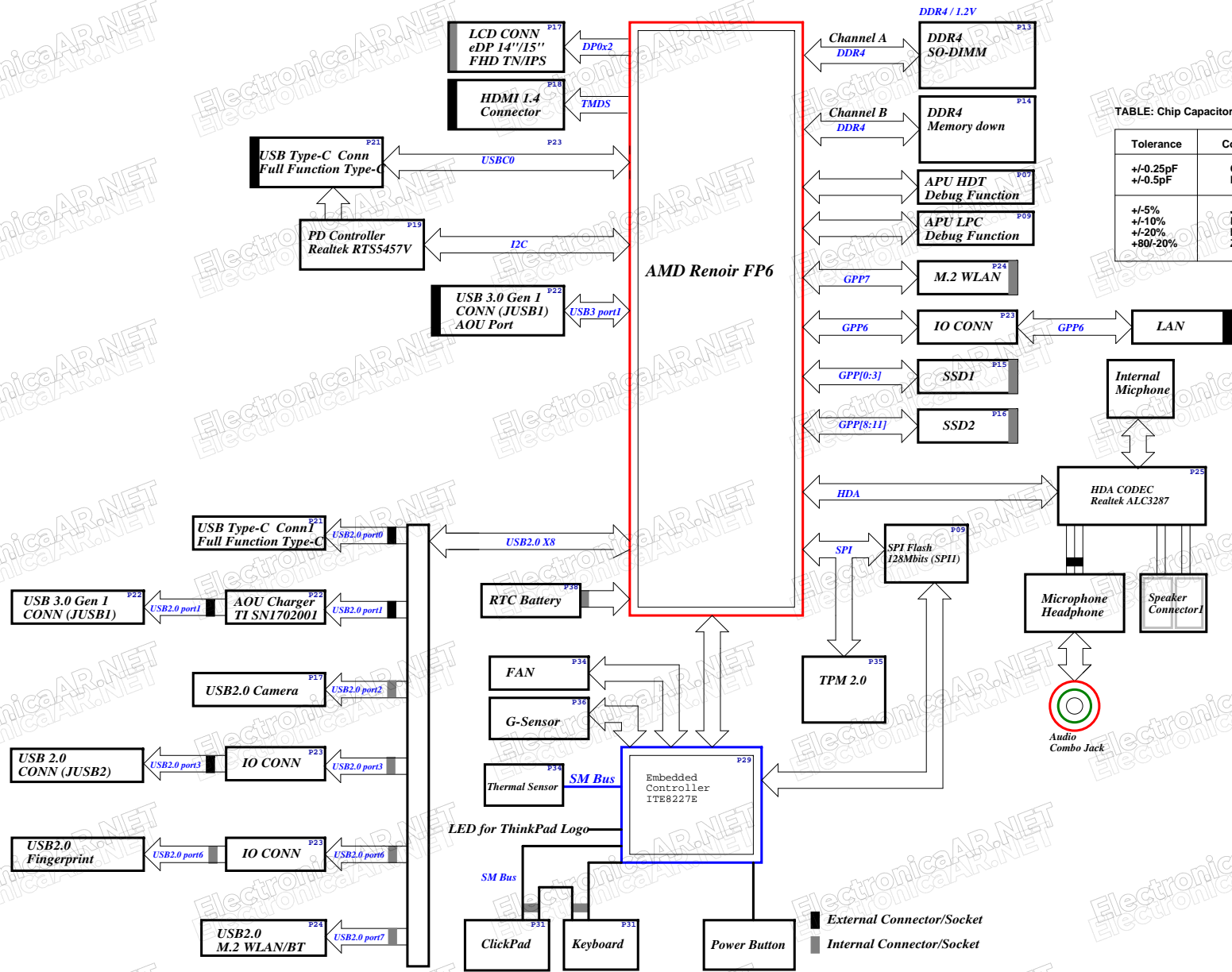


TABLE: Chip Capacitor Thermal Characteristics

		Code
-55 to 150degC	+/-30ppm/degC	NPO
-55 to 125degC	+/-15%	X7R
-55 to 105degC	+/-22%	X6S
-55 to 85degC	+/-15%	X5R

TABLE: Chip Capacitor Tolerance

Tolerance	Code
+/-0.25pF	C
+/-0.5pF	D
+/-5%	J
+/-10%	K
+/-20%	M
+80/-20%	Z

TABLE: Chip Part Dimension

Size [mm]	mm Size Code	Inch Size Code
0.40 x 0.20	0402	01005
0.60 x 0.30	0603	0201
1.00 x 0.50	1005	0402
1.60 x 0.80	1608	0603
2.00 x 1.25	2125	0805
2.00 x 1.60	2016	0806
2.50 x 2.00	2520	1008
3.20 x 1.60	3216	1206
3.20 x 2.50	3225	1210
4.50 x 1.60	4516	1806
4.50 x 2.50	4525	1810
4.50 x 3.20	4532	1812
5.00 x 2.50	5025	2010
6.40 x 3.20	6432	2512

STATE	SIGNAL	SLP_S3#	SUSP#	SLP_S5#	SYSON	EC_ON	
S0		HIGH		HIGH		ON	
S3 (Suspend to RAM)		LOW		HIGH		ON	
S4 (Suspend to Disk)		LOW		LOW		ON	
S5 (Soft OFF)		LOW		LOW		ON	

USB2 Port

Port	Device
0	USBC Type-C
1	USB3.0 port
2	Camera
3	USB2.0 Port
4	X
5	Earphone charge
6	FingerPrint
7	BT

USB3 Port

Port	Device
0	USBC Type-C
1	USB3.0 port
4	X
5	Earphone charge

PCIE Port

Port	Device
GPP0~3	SSD1
GPP4	X
GPP5	X
GPP6	LAN
GPP7	WLAN
GPP8~11	SSD2
GFX0~7	X

Voltage Rails (O --> Means ON , X --> Means OFF)

Power Plane	B9+	+3VALW +5VALW +1.8VALW +0.75VALW_VDDP	+1.2V +2.5V	+5VS +3VS +1.8VS +0.75VS_VDDP +0.6VS +VDDCR_SOC +VDDC_VDD
State				
S0	O	O	O	O
S3	O	O	O	X
S5 S4/AC Only	O	O	X	X
S5 S4 Battery only	O	X	X	X
G3 AC & Battery don't exist	X	X	X	X

SMBUS Control Table


EC	Schemic	SOURCE	PMIC	BATT	Charge	PD	APU	USB3.1 Redriver	G-Sensor	Thermal Sensor	SODIMM	CP Module
SMCLK0 SMDAT0	EC_SMB_CK0 EC_SMB_DA0	EC +3VL	V +3VL	X	X	X	X	X	X	X	X	X
SMCLK1 SMDAT1	EC_SMB_CK1 EC_SMB_DA1	EC +3VL	X	V +3VL	V +3VL	X	X	X	X	X	X	X
SMCLK2 SMDAT2	EC_SMB_CK2 EC_SMB_DA2	EC +3VL	X	X	X	V +3VL	X	X	X	X	X	X
SMCLK3 SMDAT3	EC_SMB_CK3 EC_SMB_DA3	EC +3VS	X	X	X	X	V +3VS	X	X	X	X	X
SMCLK4 SMDAT4	EC_SMB_CK4 EC_SMB_DA4	EC +3VS	X	X	X	X	X	X	V +3VS	V +3VS	X	X
	APU_SMB0CLK APU_SMB0DATA	APU +3VS	X	X	X	X	X	X	X	X	V +3VS	X
	APU_SMB1CLK APU_SMB1DATA	APU +3VALW	X	X	X	X	X	X	X	X	X	V +3VS

PD Controller I2C

	SOURCE	APU	Type C Redriver
REPETER_SCL REPETER_SDA	PD +LDO_3V3	V +1.8VALW	V +LDO_3V3







BOM Structure Table

BOM Structure	NOTE
HDT#	For HDT AMD debug port
LPC#	For LPC AMD debug port
TPM#	Trusted Platform Module(TPM)
CD#	COST DOWN
MB#	ME Connector
EMC#	For EMI function
EMC_NS#	EMC Reserves
RF#	For RF function
RF_NS#	RF Reserves

 S IC RYZEN 3 100-00000085-00 2.7G PR S4000A9W02	 S IC RYZEN 5 100-00000084-00 2.375G PR S4000A9W03	 S IC RYZEN 7 100-00000083-00 2G BGA PR S4000A9W03	 S IC RYZEN 7 100-00000082-00 1.8G S4000A9W02
---	---	---	--

DRAM PN				BOARD ID		CONFIGURE		UZQ		T7 Pin				CHB SDF/DDP			
UD1 M4G-B MT40A512M16TB-062E-J S400009F500	UD2 M4G-B MT40A512M16TB-062E-J S400009F500	UD3 M4G-B MT40A512M16TB-062E-J S400009F500	UD4 M4G-B MT40A512M16TB-062E-J S400009F500	RC111 M4G-B 10K_D402_5% SD0281002BT	RC112 M4G-B 10K_D402_5% SD0281002BT	RC115 M4G-B 10K_D402_5% SD0281002BT	Micron D4G	RD47 M4G-B 0_D402_5% SD028000YT	RD48 M4G-B 0_D402_5% SD028000YT	RD49 M4G-B 0_D402_5% SD028000YT	RD50 M4G-B 0_D402_5% SD028000YT		RD17 M4G-B 0_D402_5% SD028000YT	SDP			
UD1 M8G-B MT40A1G16KD-062E-E S4000A0K10	UD2 M8G-B MT40A1G16KD-062E-E S4000A0K10	UD3 M8G-B MT40A1G16KD-062E-E S4000A0K10	UD4 M8G-B MT40A1G16KD-062E-E S4000A0K10	RC110 M8G-B 10K_D402_5% SD0281002BT	RC113 M8G-B 10K_D402_5% SD0281002BT	RC114 M8G-B 10K_D402_5% SD0281002BT	Micron D8G	RD47 M8G-B 0_D402_5% SD028000YT	RD48 M8G-B 0_D402_5% SD028000YT	RD49 M8G-B 0_D402_5% SD028000YT	RD50 M8G-B 0_D402_5% SD028000YT		RD17 M8G-B 0_D402_5% SD028000YT	SDP			
UD1 H8G-B HSAN8GNCJR-XNC S4000AC720	UD2 H8G-B HSAN8GNCJR-XNC S4000AC720	UD3 H8G-B HSAN8GNCJR-XNC S4000AC720	UD4 H8G-B HSAN8GNCJR-XNC S4000AC720	RC111 H8G-B 10K_D402_5% SD0281002BT	RC115 H8G-B 10K_D402_5% SD0281002BT	RC114 H8G-B 10K_D402_5% SD0281002BT	Hylix D4G	RD47 H8G-B 0_D402_5% SD028000YT	RD48 H8G-B 0_D402_5% SD028000YT	RD49 H8G-B 0_D402_5% SD028000YT	RD50 H8G-B 0_D402_5% SD028000YT		RD17 H8G-B 0_D402_5% SD028000YT	SDP			
UD1 H8G-B HSAN8GNCMR-XNC S4000AC300	UD2 H8G-B HSAN8GNCMR-XNC S4000AC300	UD3 H8G-B HSAN8GNCMR-XNC S4000AC300	UD4 H8G-B HSAN8GNCMR-XNC S4000AC300	RC110 H8G-B 10K_D402_5% SD0281002BT	RC113 H8G-B 10K_D402_5% SD0281002BT	RC115 H8G-B 10K_D402_5% SD0281002BT	Hylix D8G	RD47 H8G-B 240_D402_1% SD000009YT	RD48 H8G-B 240_D402_1% SD000009YT	RD49 H8G-B 240_D402_1% SD000009YT	RD50 H8G-B 240_D402_1% SD000009YT	RD41 H8G-B 0_D402_5% SD028000YT	RD45 H8G-B 0_D402_5% SD028000YT	RD46 H8G-B 0_D402_5% SD028000YT	RD47 H8G-B 0_D402_5% SD028000YT	RD48 H8G-B 0_D402_5% SD000151YT	DDP
UD1 S4G-B K4A8G16SWC-BCWE S4000AC300	UD2 S4G-B K4A8G16SWC-BCWE S4000AC300	UD3 S4G-B K4A8G16SWC-BCWE S4000AC300	UD4 S4G-B K4A8G16SWC-BCWE S4000AC300	RC111 S4G-B 10K_D402_5% SD0281002BT	RC113 S4G-B 10K_D402_5% SD0281002BT	RC115 S4G-B 10K_D402_5% SD0281002BT	SAMSUNG D4G	RD47 S4G-B 0_D402_5% SD028000YT	RD48 S4G-B 0_D402_5% SD028000YT	RD49 S4G-B 0_D402_5% SD028000YT	RD50 S4G-B 0_D402_5% SD028000YT		RD17 S4G-B 0_D402_5% SD028000YT	SDP			
UD1 S8G-B K4A8G16SWA-BCWE S4000AC300	UD2 S8G-B K4A8G16SWA-BCWE S4000AC300	UD3 S8G-B K4A8G16SWA-BCWE S4000AC300	UD4 S8G-B K4A8G16SWA-BCWE S4000AC300	RC111 S8G-B 10K_D402_5% SD0281002BT	RC113 S8G-B 10K_D402_5% SD0281002BT	RC115 S8G-B 10K_D402_5% SD0281002BT	SAMSUNG D8G	RD47 S8G-B 0_D402_5% SD028000YT	RD48 S8G-B 0_D402_5% SD028000YT	RD49 S8G-B 0_D402_5% SD028000YT	RD50 S8G-B 0_D402_5% SD028000YT		RD17 S8G-B 0_D402_5% SD028000YT	SDP			

PCB	
 PCB THU NM-C771 REV0 MB S421760100	 PCB THU NM-C771 REV0 MB S421760100

X76					
 MICRON D4G X74801001	 MICRON D8G X74801003	 HYNIX D4G X74801002	 HYNIX D8G X74801001	 SAMSUNG D4G X74801004	 SAMSUNG D8G X74801002

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REV D			R14/R15 AND GEN4
Revit			2019/06/21 09:00 4 of 50

M.2 SSD1

LAN

WLAN

M.2 SSD2

M.2 SSD1

LAN

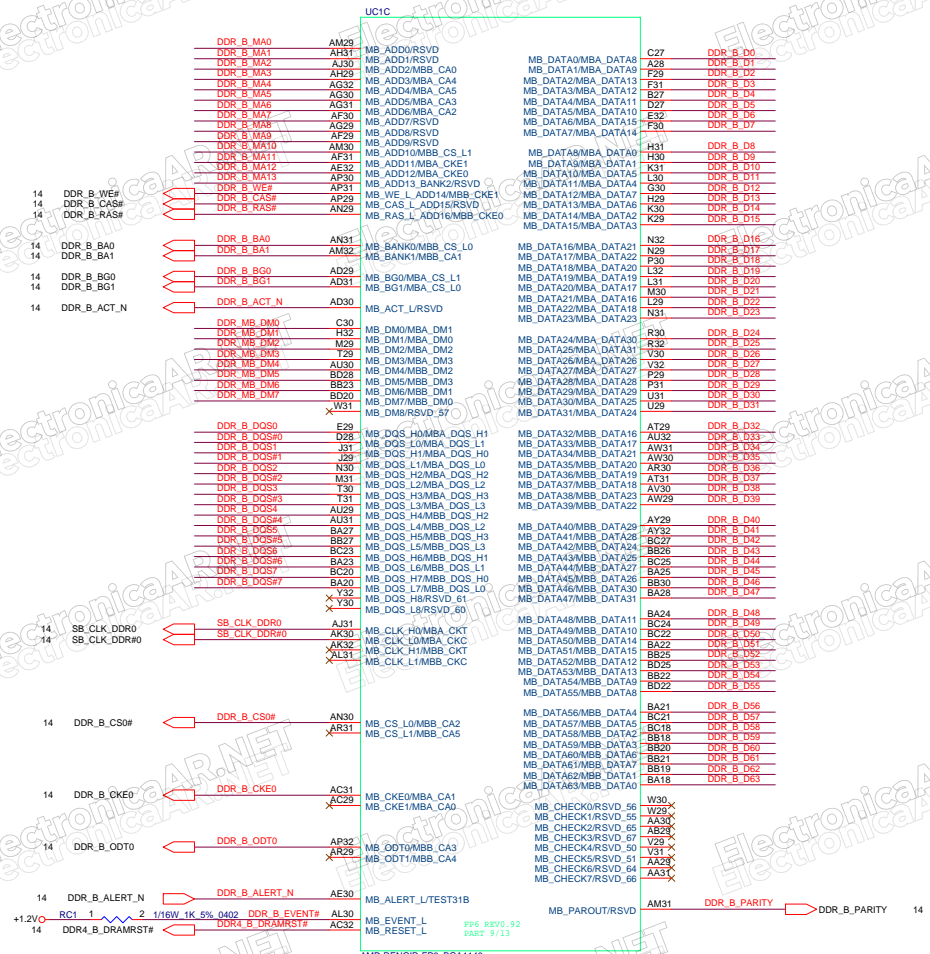
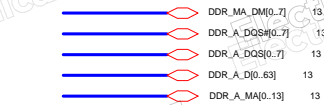
WLAN

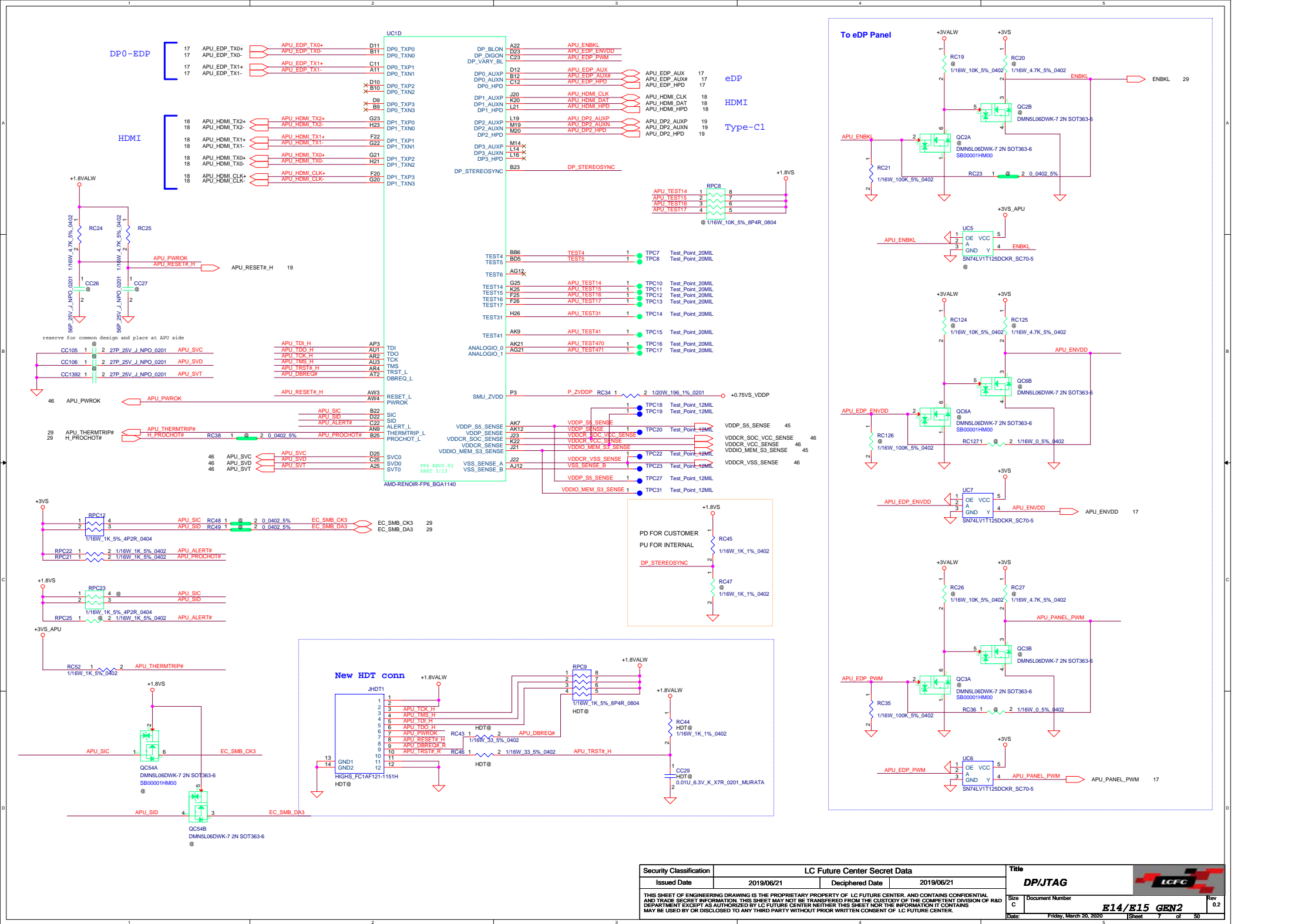
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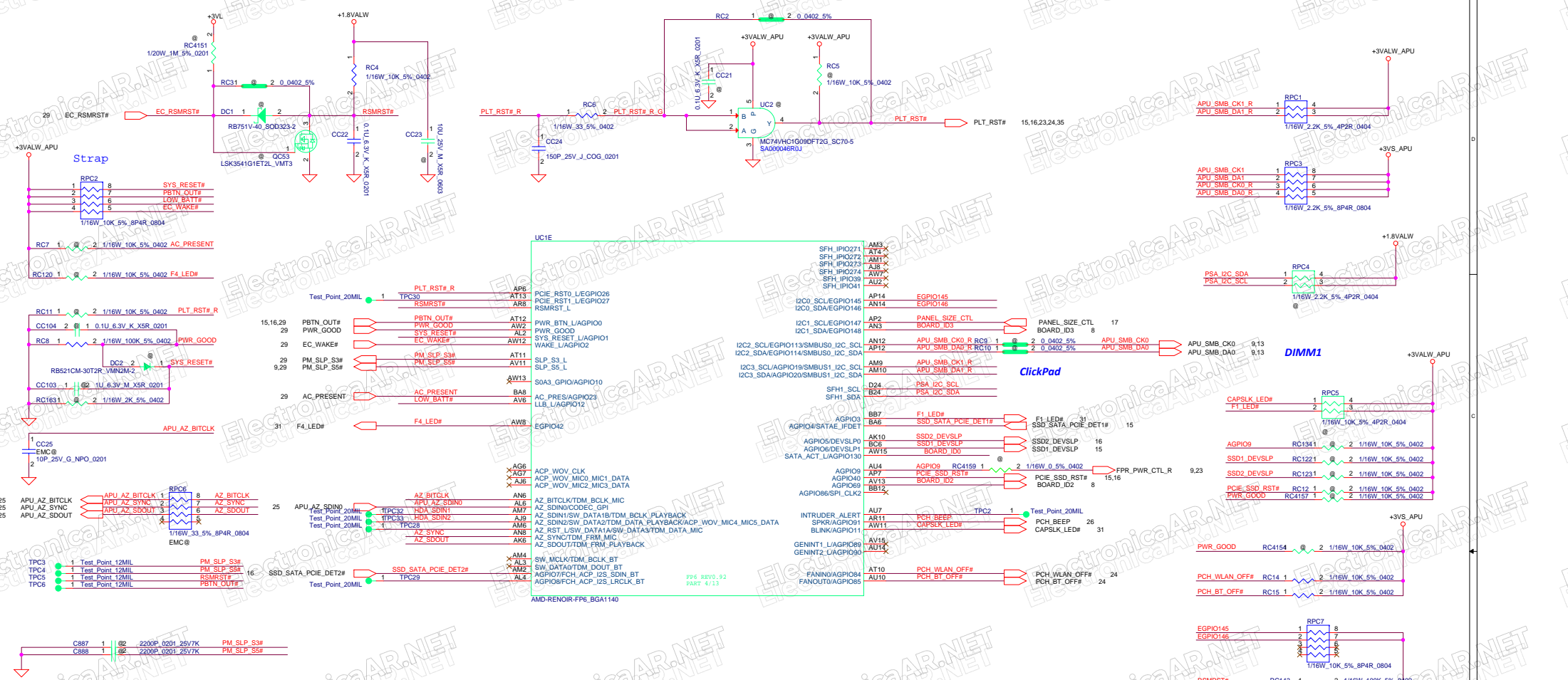
PP6 REV0.92
PART 2/13

AMD-RENOIR-PP6_BGA1140

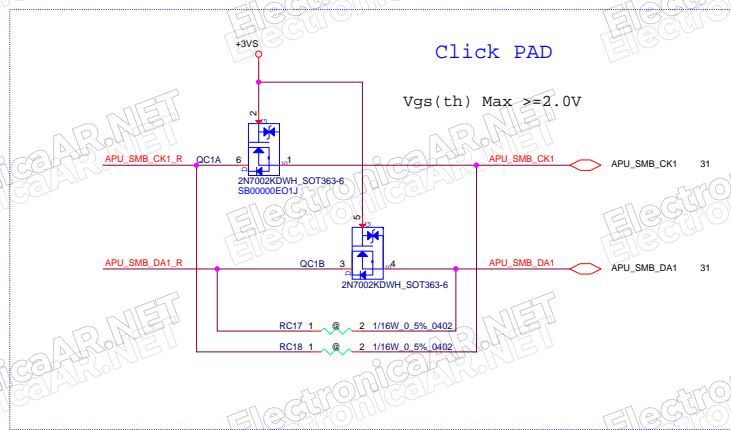
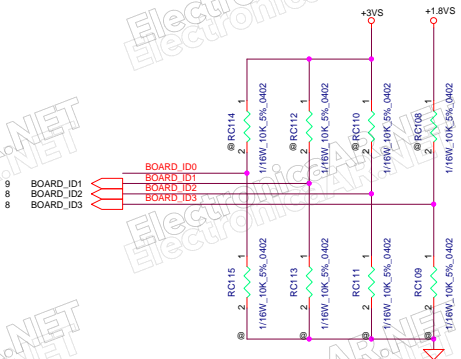
Close to APU

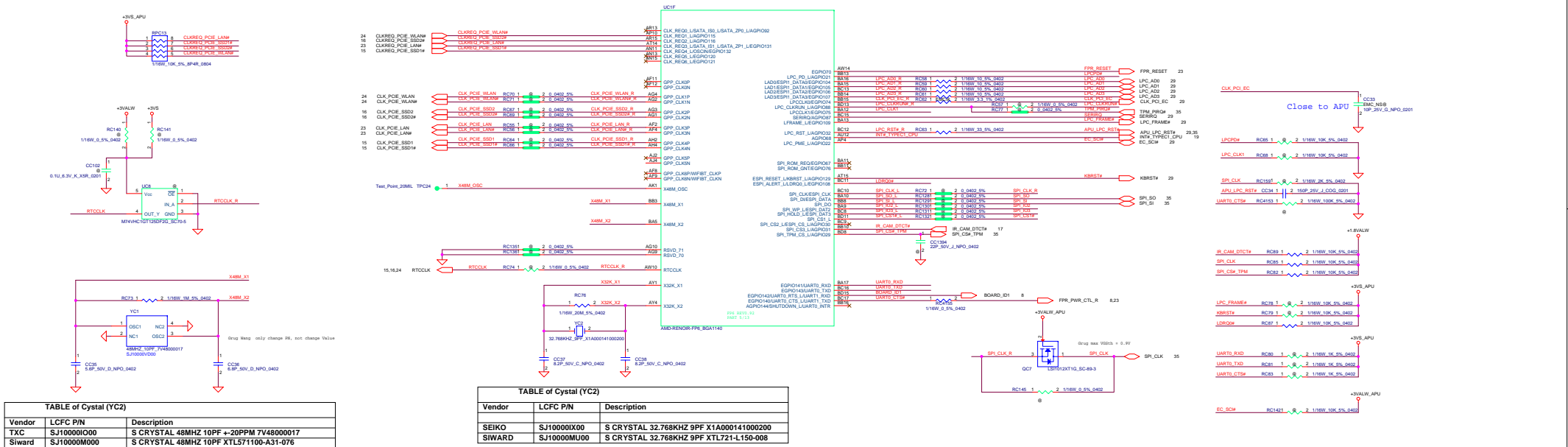






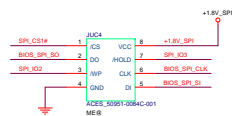
Board ID	Description	Stuff R
000	Samsung 4Gb 3200 MT/s	RC111 RC113 RC115
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010	Samsung 8Gb 3200 MT/s	RC111 RC112 RC115
011	Hynix 8Gb 3200 MT/s	RC111 RC112 RC114
100	Bynix 8Gb 3200 MT/s	RC110 RC113 RC115
101	Micron 8Gb 3200 MT/s	RC110 RC113 RC114
110	Reserved	RC110 RC112 RC115
111	Reserved	RC110 RC112 RC114
Board_ID3	0 Reserved	RC109
1	Reserved	RC108



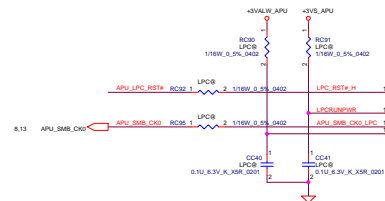


LPC debug connector 20PIN, only reserve test point

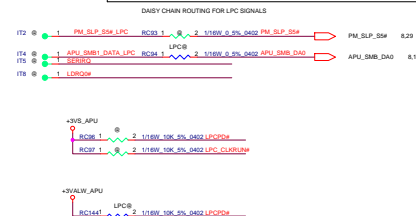
As below is CRB circuit

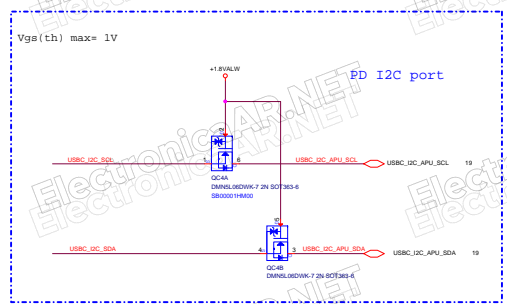


LPC ROM EMULATOR HEADER

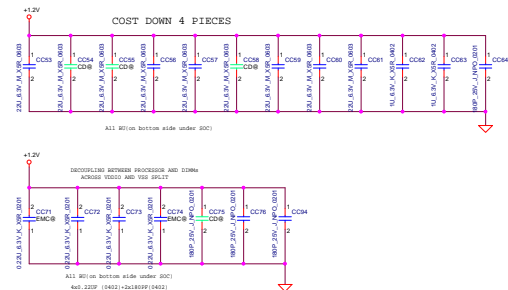



PIN4 should be removed as a Key

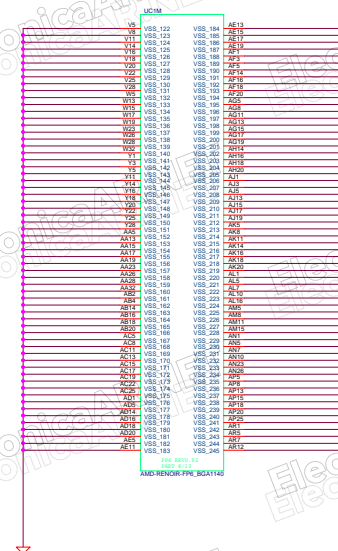
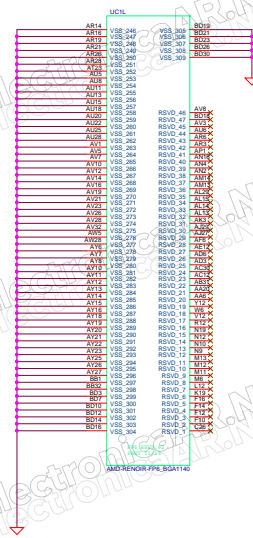


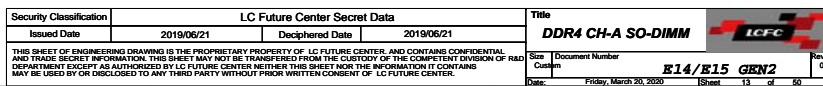


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		Date Friday, March 26, 2020	Global 10 02



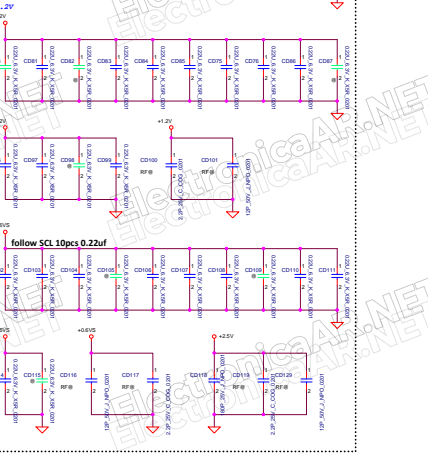
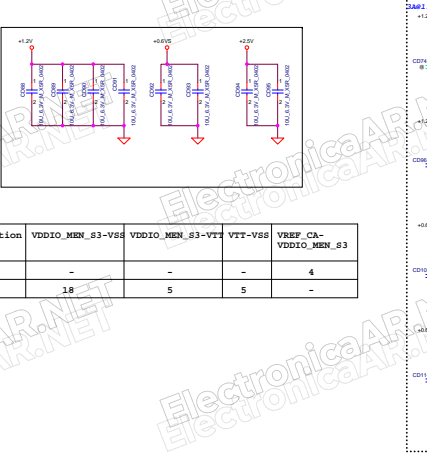
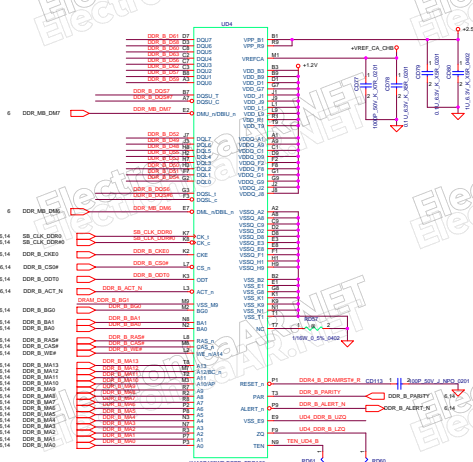
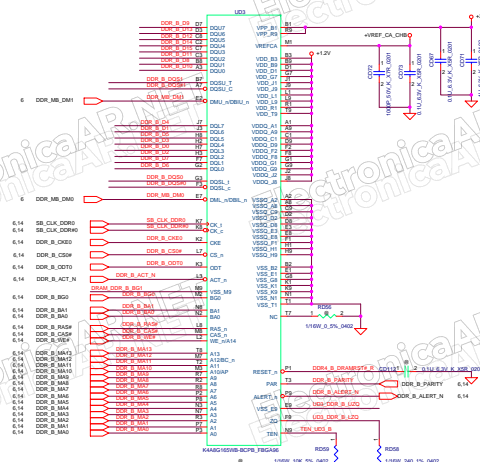
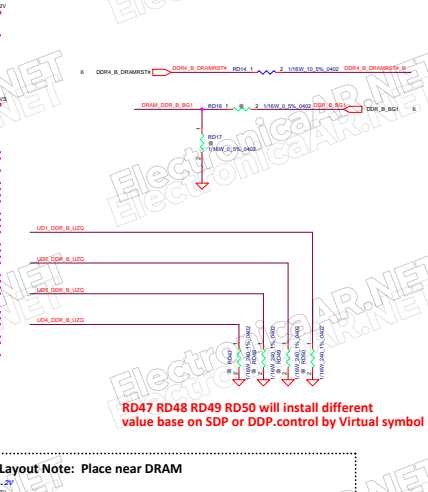
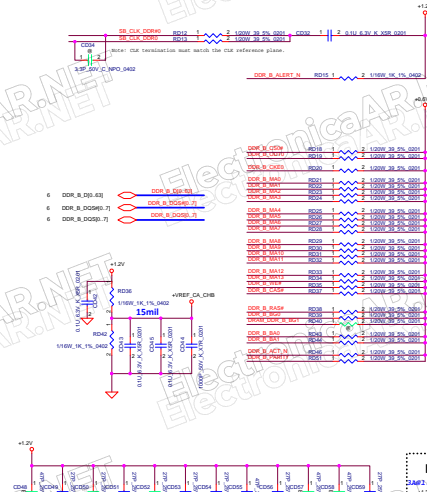
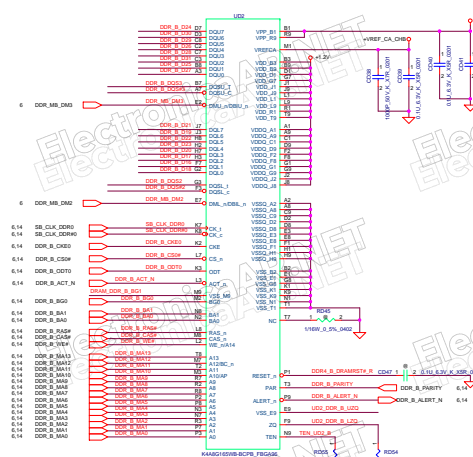
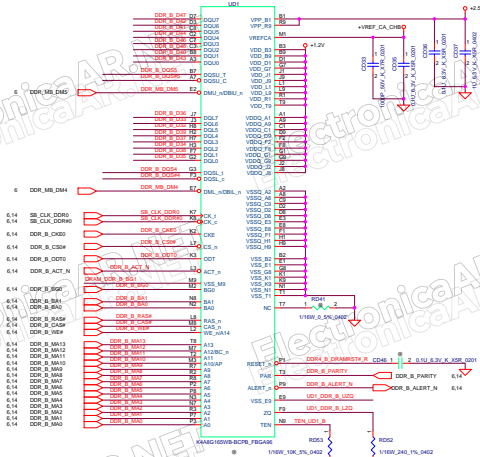
Security Classification	LC Future Center Secret Data			Title	Power		
Issued Date	2019/06/21	Deciphered Date	2019/06/21	Doc ID	E14/E15 GEN2		
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				Date	Friday, March 26, 2020		





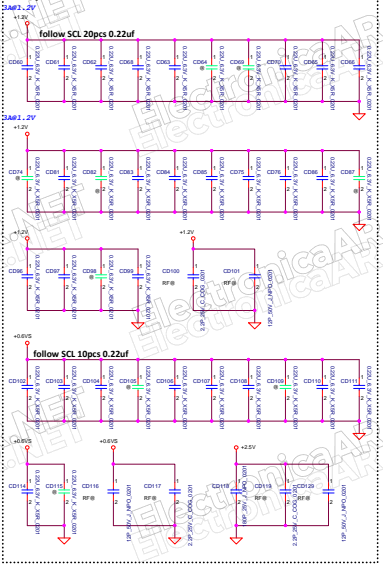
8Gb SDP
16Gb DDP

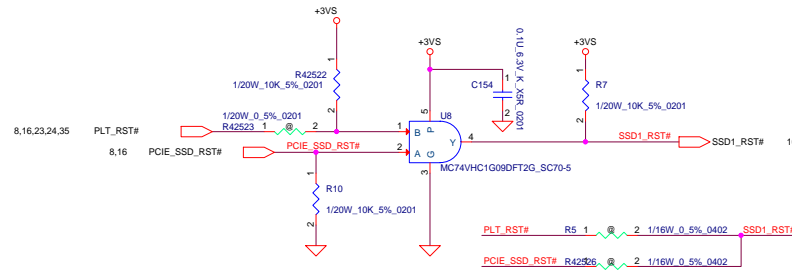
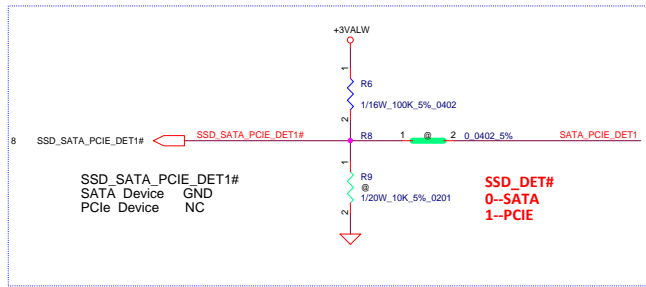
8Gb SDP
16Gb DDP



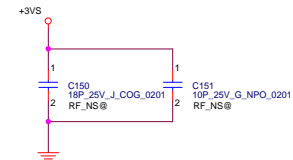
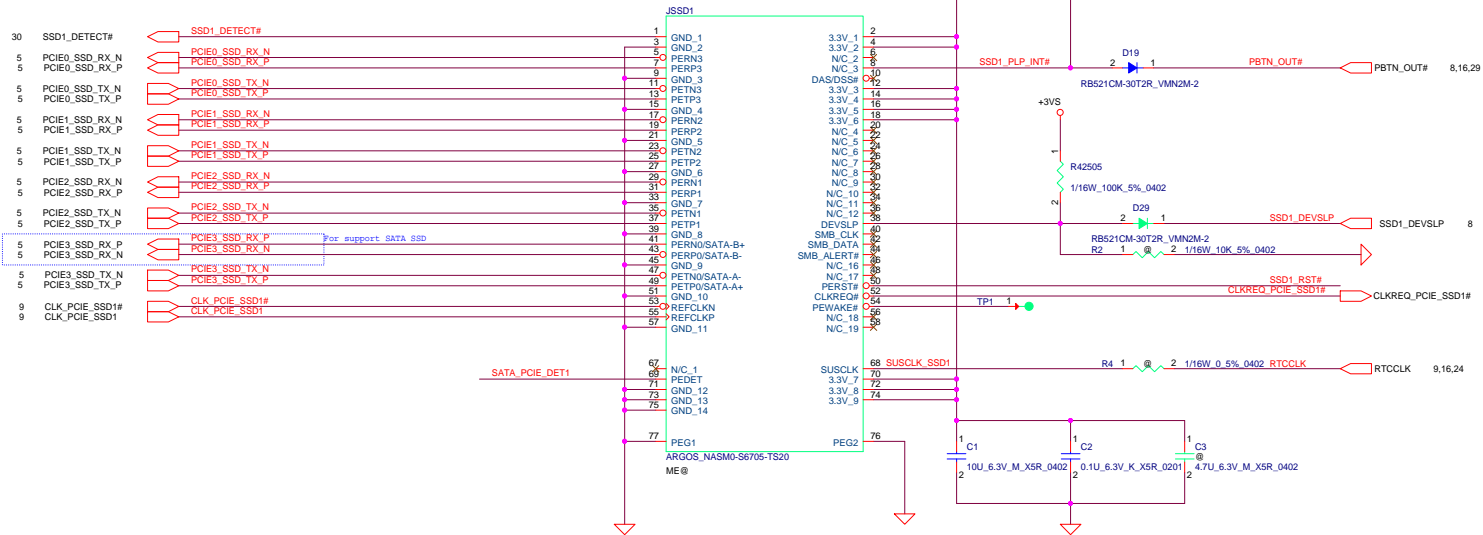
Capacitor Value	Configuration	VDDIO_MEN_S3-VSS	VDDIO_MEN_S3-VTT	VTT-VSS	VREF_CA-VDDIO_MEN_S3
5.1uF	0402 XSR	SRx16	-	-	-
0.22uF	0402 XSR	SRx16	18	5	5

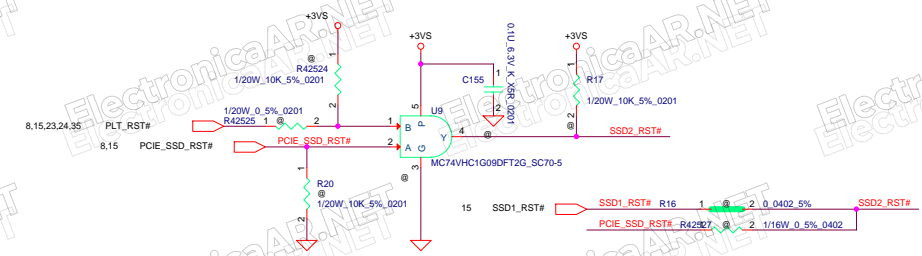
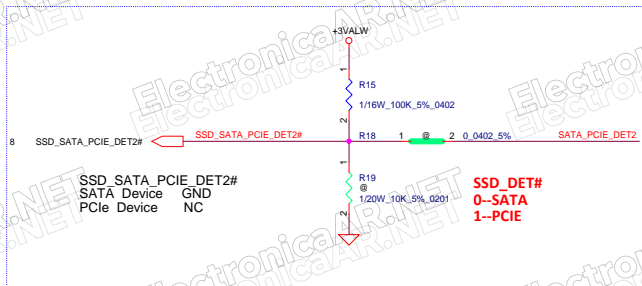
Layout Note: Place near DRAM



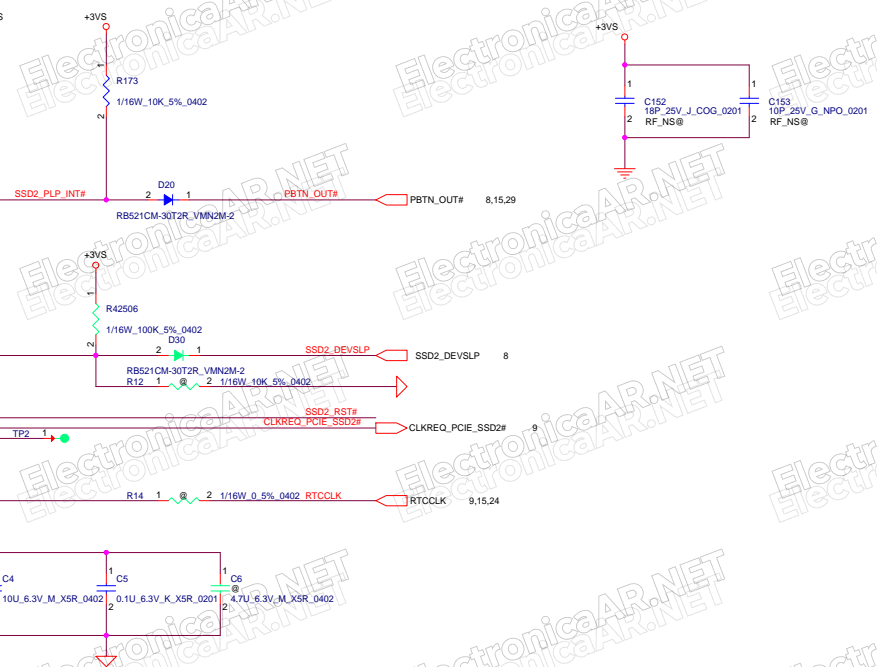
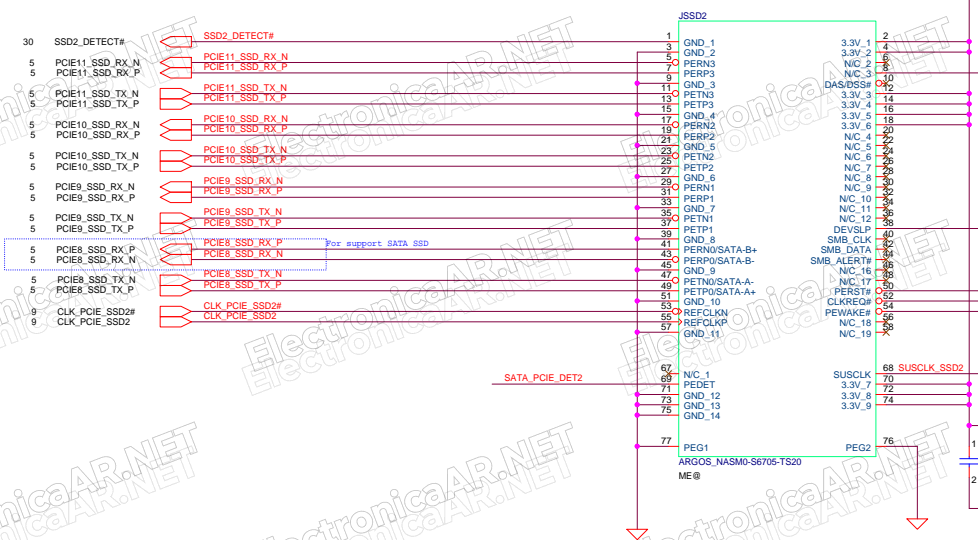


M.2 SSD(M TYPE)





M.2 SSD(M TYPE)



LCDVDD Circuit

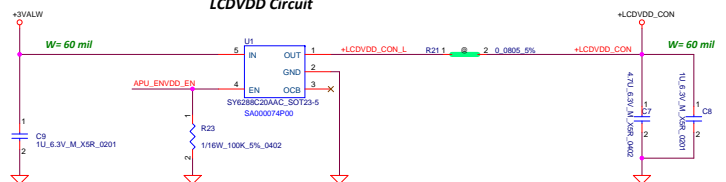
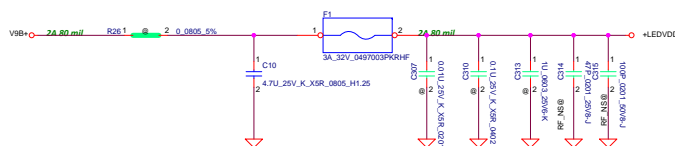


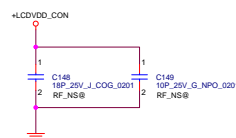
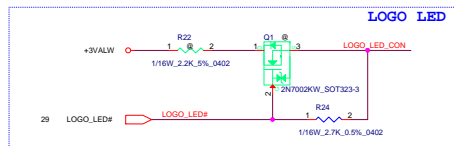
TABLE of POWER SWITCH (U1)

Vendor	LCFC P/N	Description
SILERGY	SA000074P00	S IC SY6288C20AAC SOT23 5P POWER SWITCH
GMT	SA00007S700	S IC G5247T11U SOT-23 5P POWER SWITCH

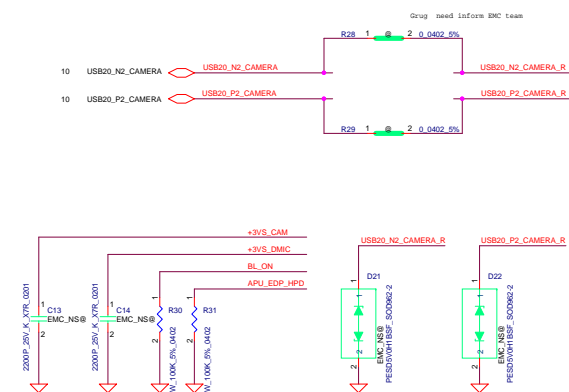
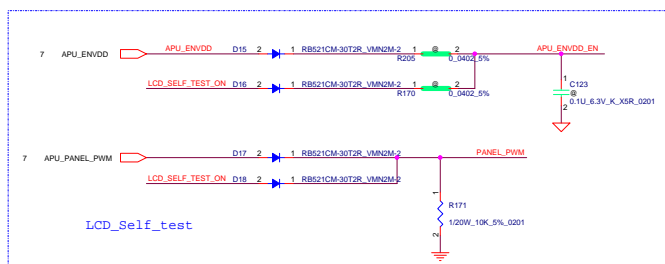


Board ID defined by EDP cable

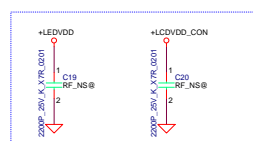
	14'	15'
SIZE_CTL	leave as NC	Connect to GND by EDP cable



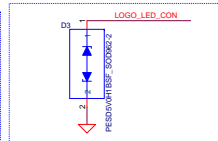
LCD Self test



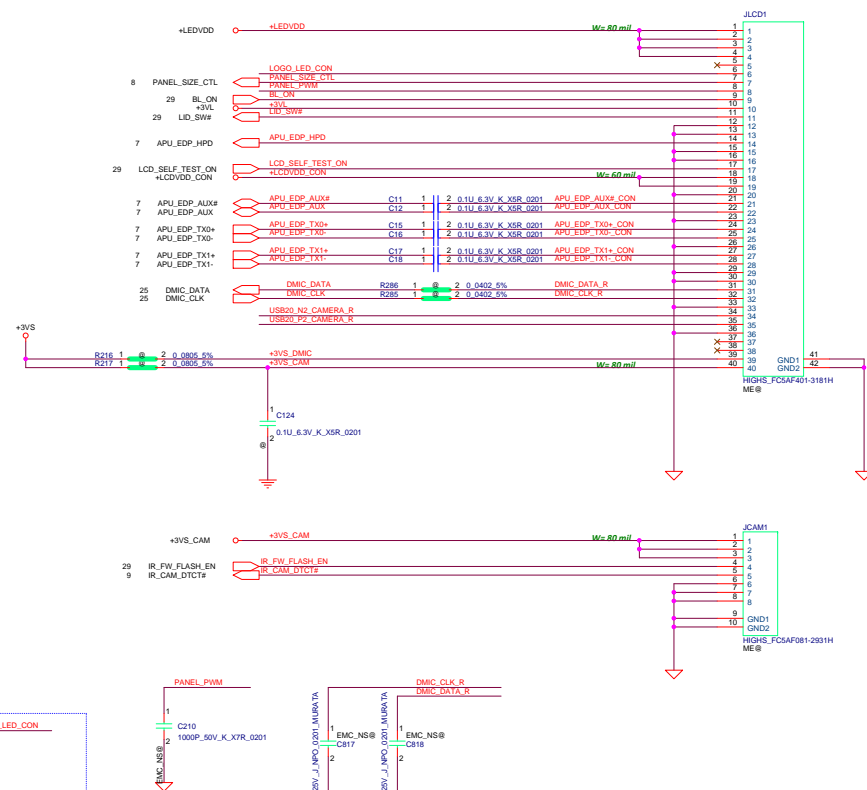
EMC request. Close to JLCD1




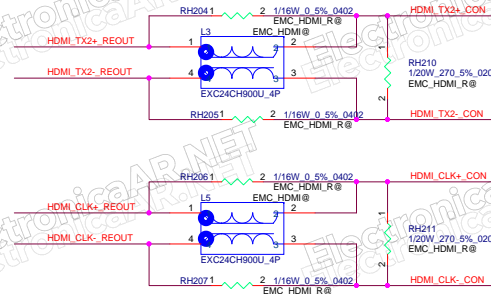
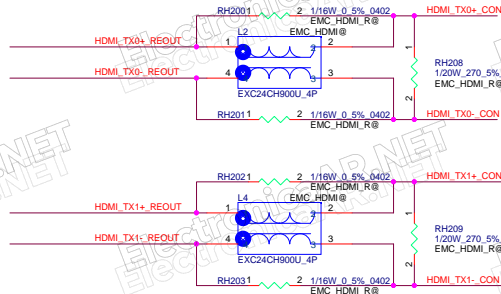
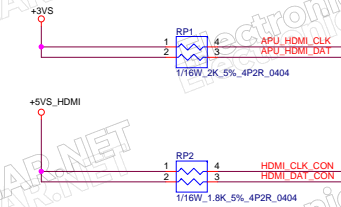
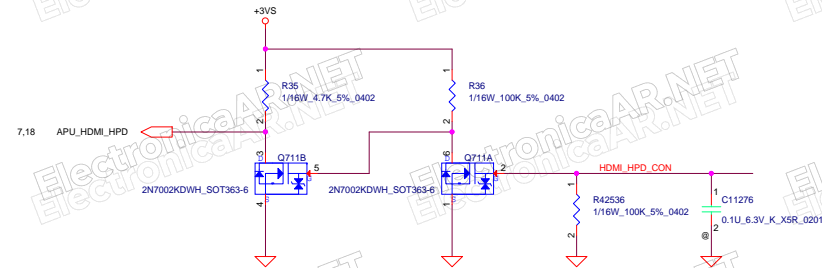
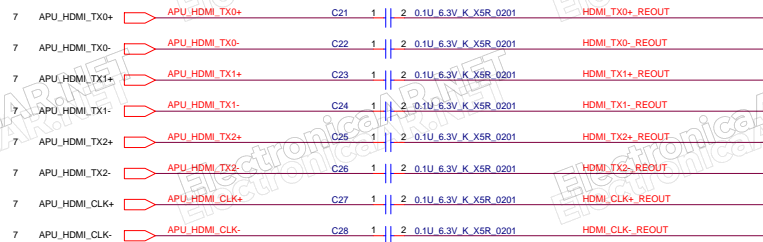
ESD request



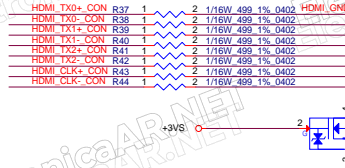
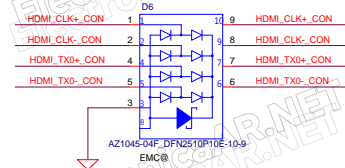
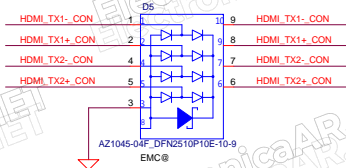
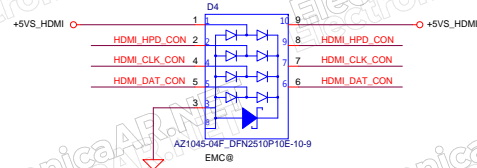
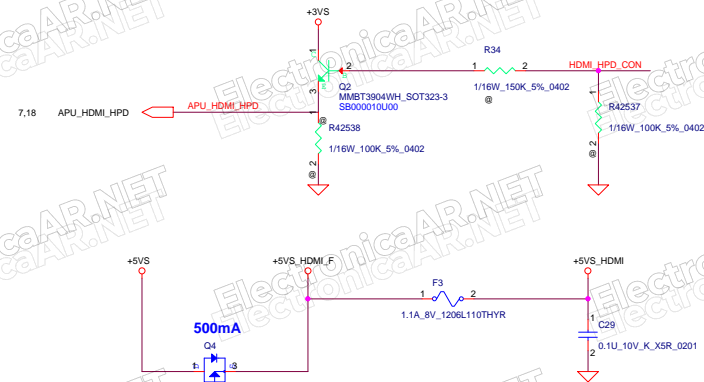
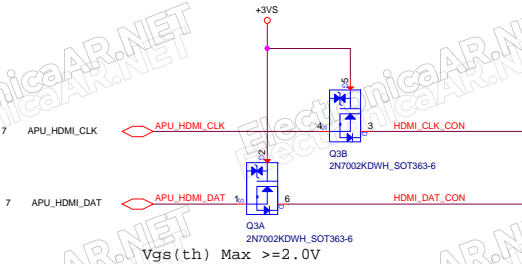
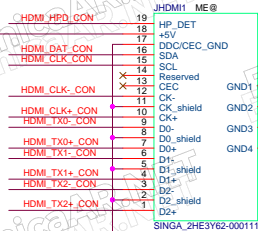
eDP/CAMERA/DMIC/LOGO-LED CONN.



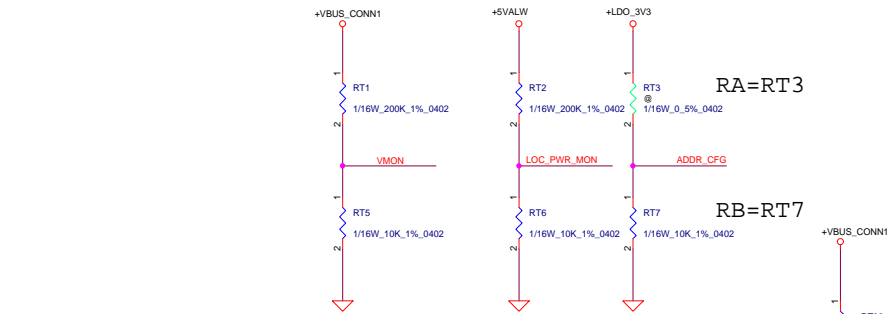
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Issued Date	2019/06/21	Deciphered Date	2019/06/21	Size	Document Number	No
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			Date	Friday, March 20, 2020 8:02 AM		



HDMI CONN.

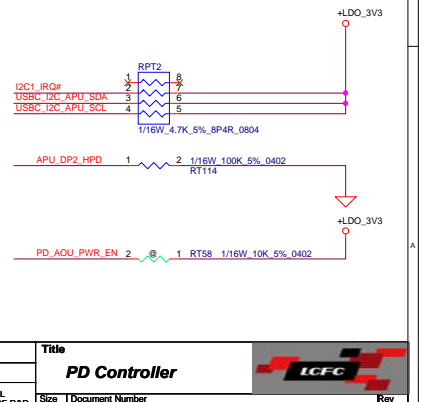
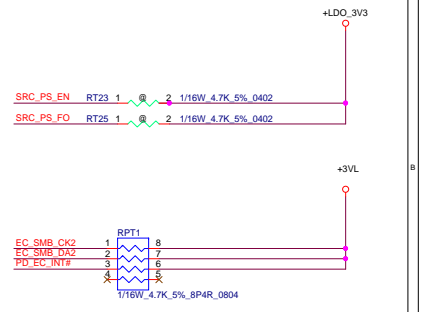
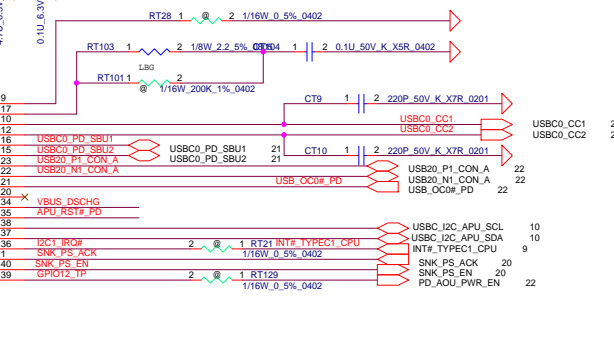
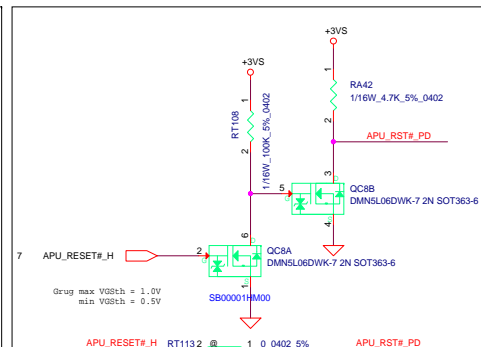
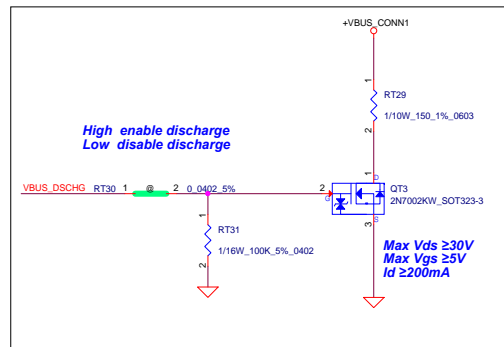
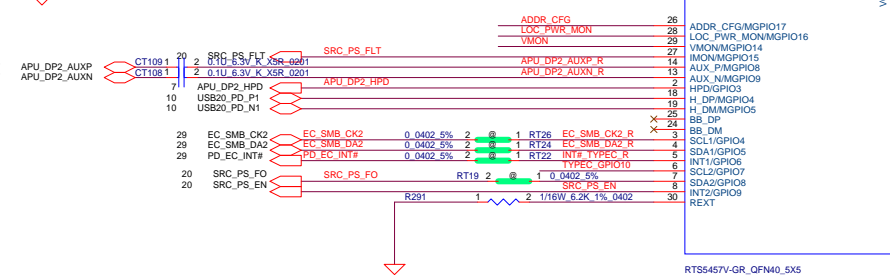
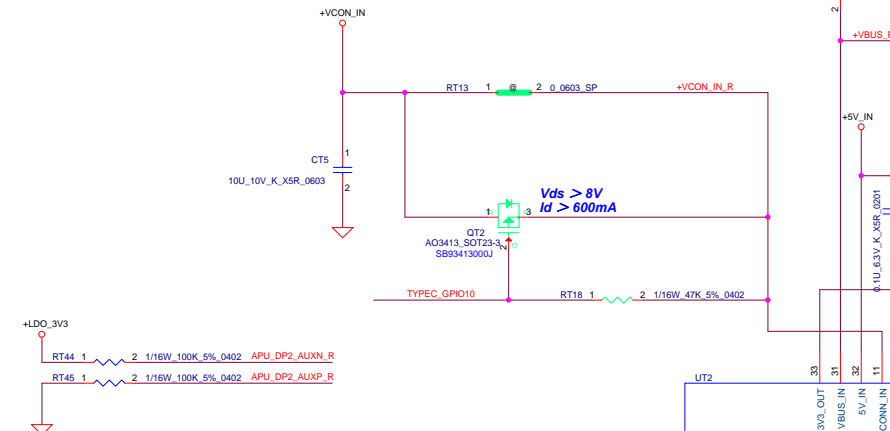
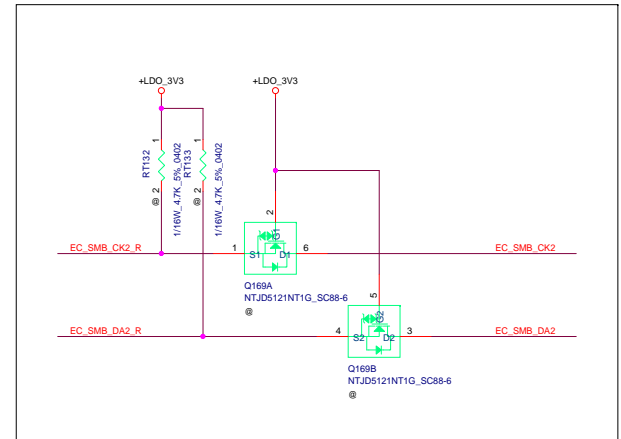


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Issued Date	2019/06/21	Deciphered Date	2019/06/21	Size	Document Number
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				E14/E15 GEN2	Rev 0.2
				Friday, March 20, 2020	Sheet 18 of 50

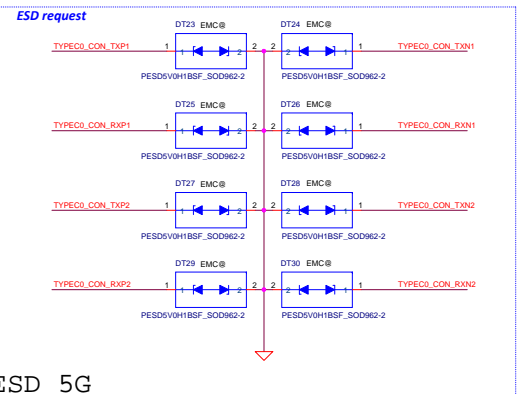
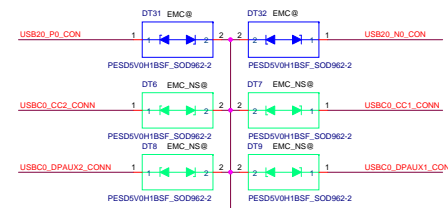
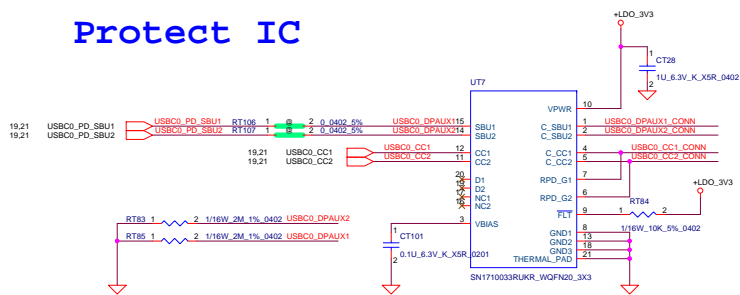


Slave Addr	Ra 5%	Rb 5%	
addr0:0xCC	NC	10K	<0.2V
addr1:0xCE	75K	10K	>=0.2V&&<0.6V
addr2:0xD0	33K	10K	>=0.6V&&<1.0V
addr3:0xD2	10K	10K	>=1.0V

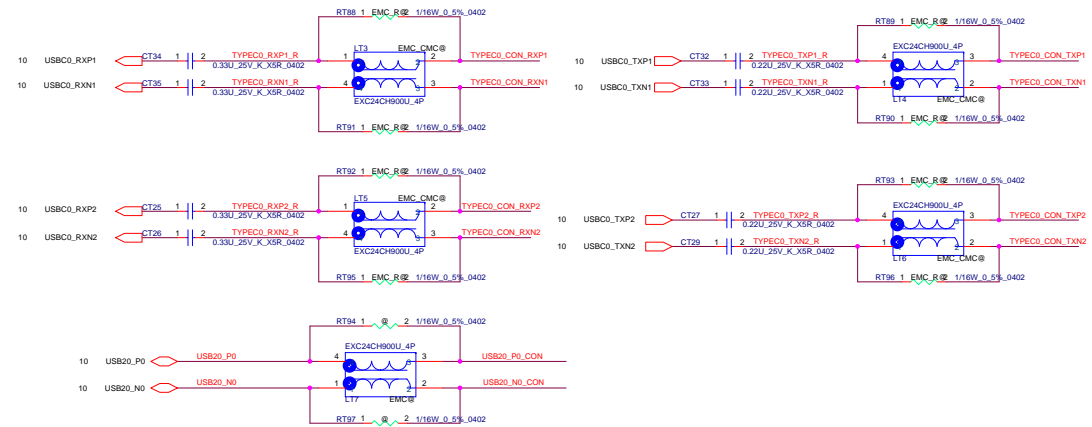
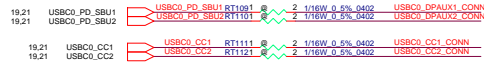
It's is used for SMBUS slave addr0/1/2/3 setting during power on initialization



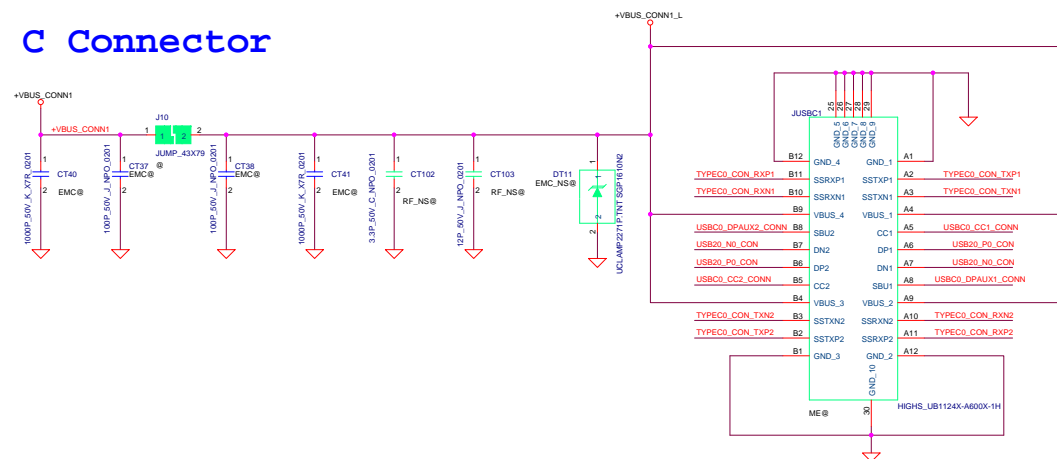
Protect IC




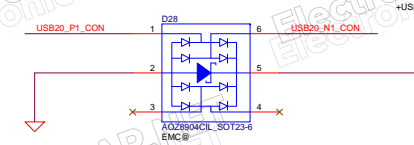
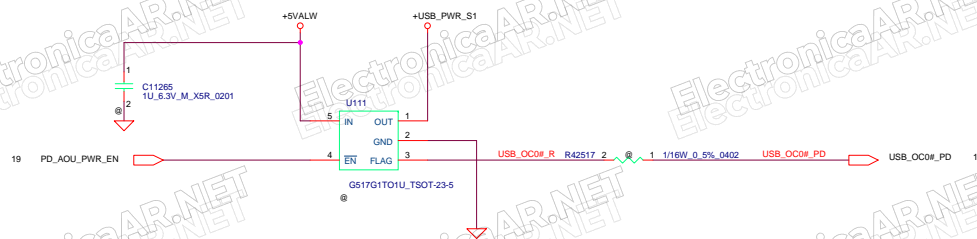
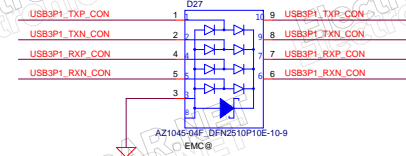
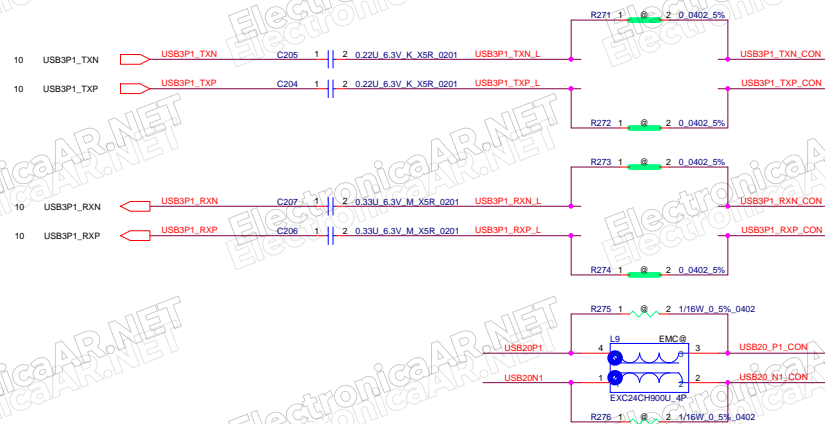
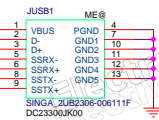
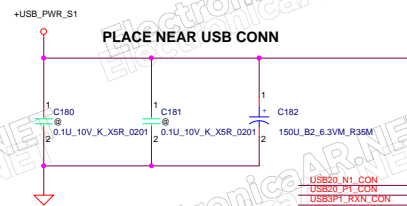
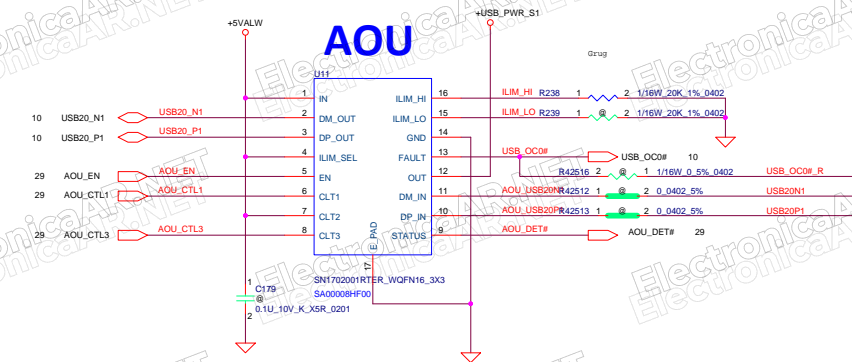
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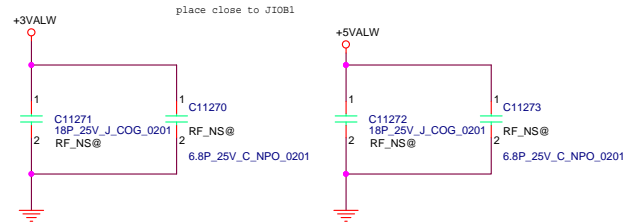
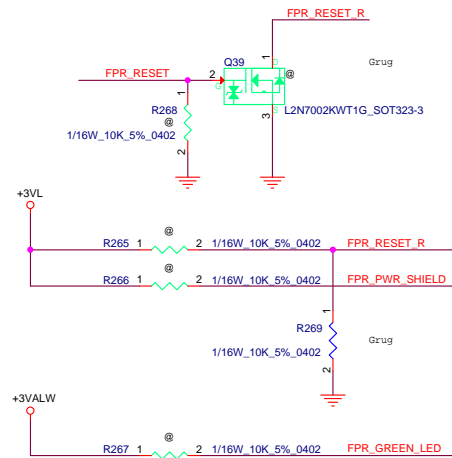


Type C Connector

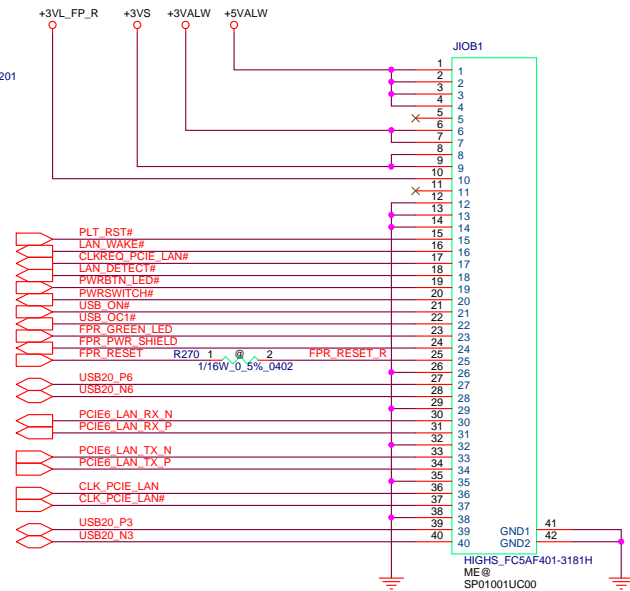


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Size				Document Number	Rev
Custom				E14/E15 GEN 21	0.2
Date:				Friday, March 20, 2020	Sheet 1 of 50





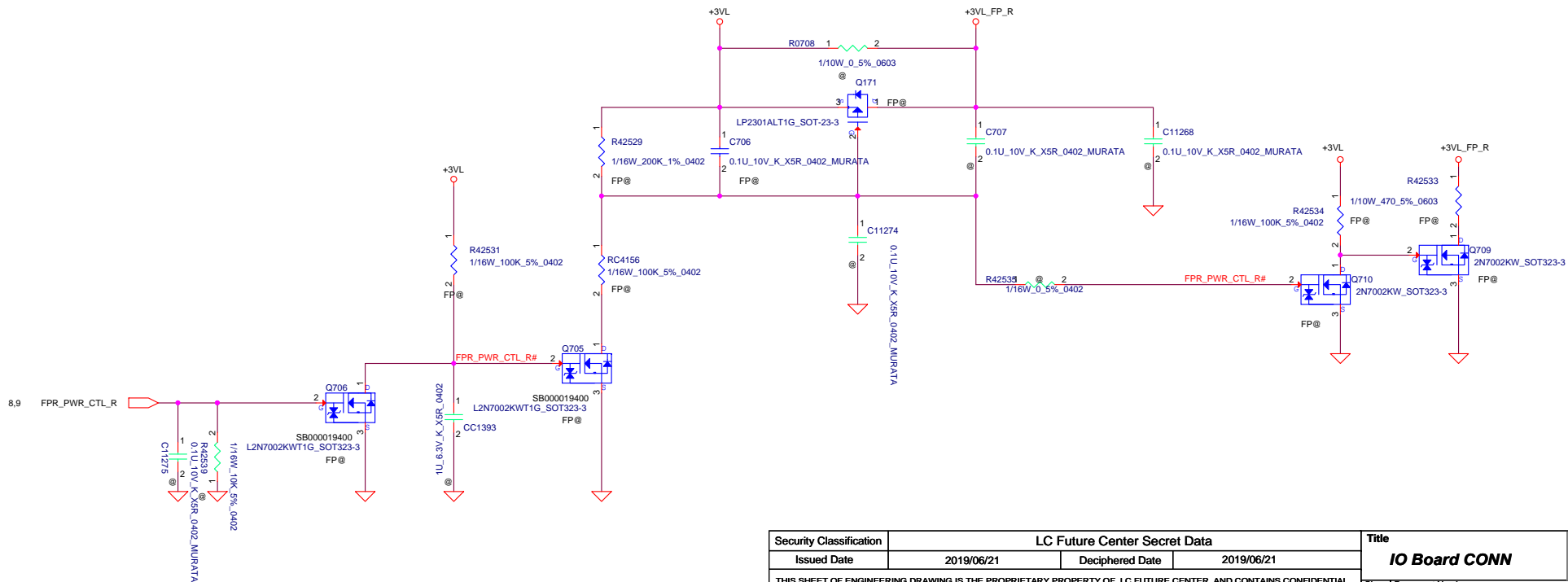
IO_40_Pin conn




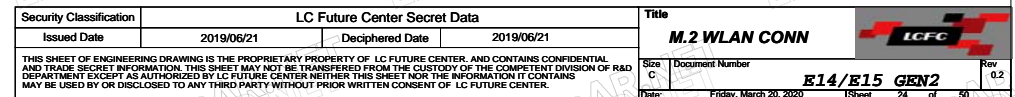
Finger Printer

GBE LAN PHY

USB 2.0

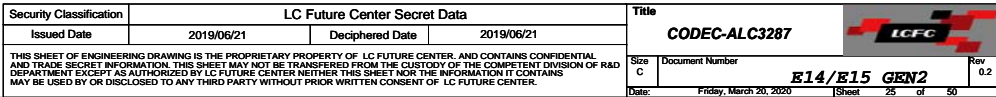


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Custom								0.2	
Date:		Friday, March 20, 2020				Sheet		23 of 50	

AOAC Reserve

The diagram shows a 5V voltage regulator circuit. The input is labeled +5VS and the output is labeled +5VS_CLASSD. A resistor RA1 with a value of 0.0805 5% is connected between the input and the output. Four electrolytic capacitors, CA1, CA2, CA3, and CA4, are connected in parallel between the input and output rails. Each capacitor has a value of 10µ 6.3V M X9R 0402. The capacitors are connected to the input rail at pin 1 and the output rail at pin 2. The output rail is also connected to a ground symbol.

Group need change to 2.2uF common design



PC Beep

EC Beep

PCH Beep

PCH_BEEP

HP JACK IN

EC_MUTE#

JSENSE

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RA21

RA23

RA25

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RA282

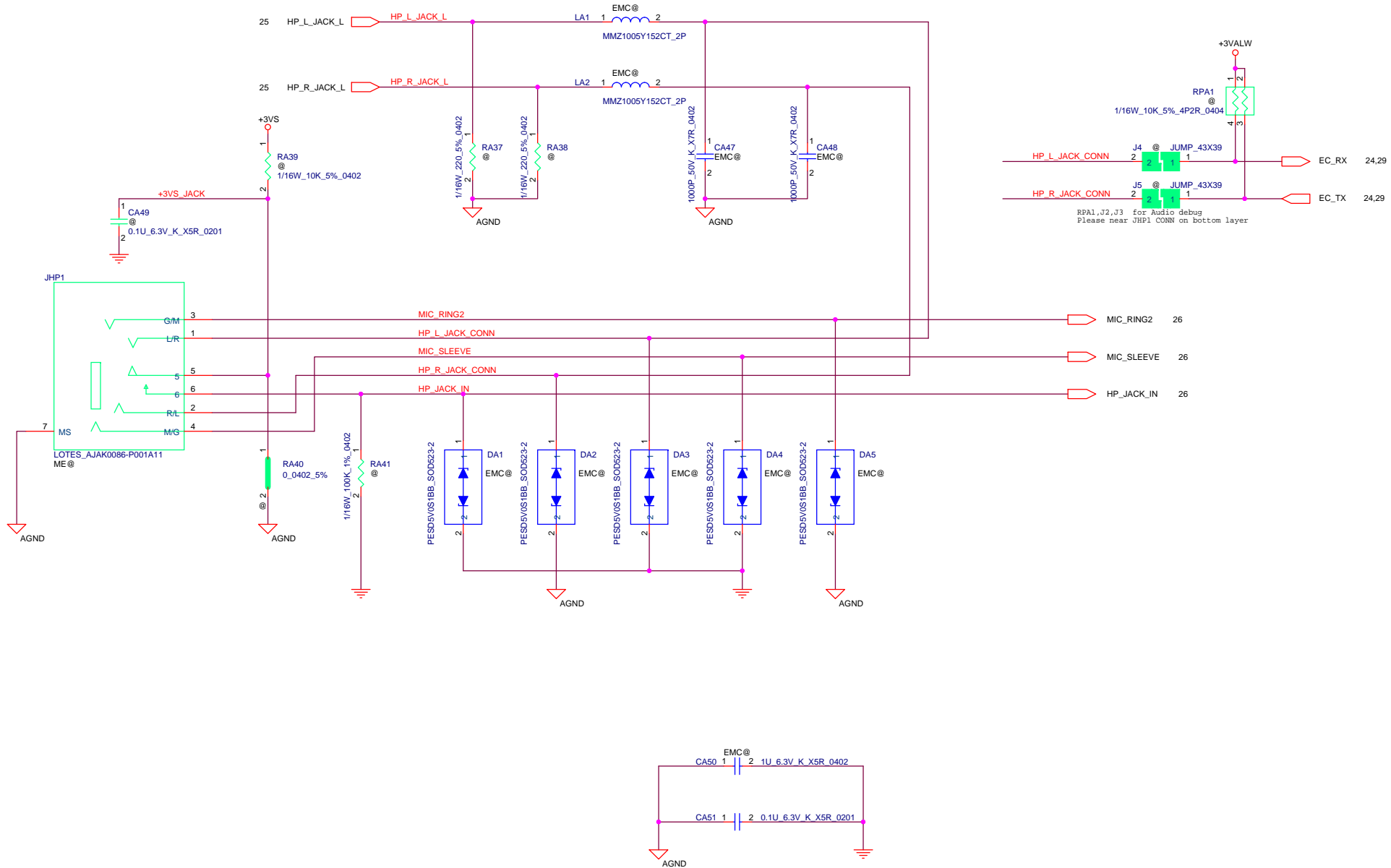
RA283

RA284

RA285

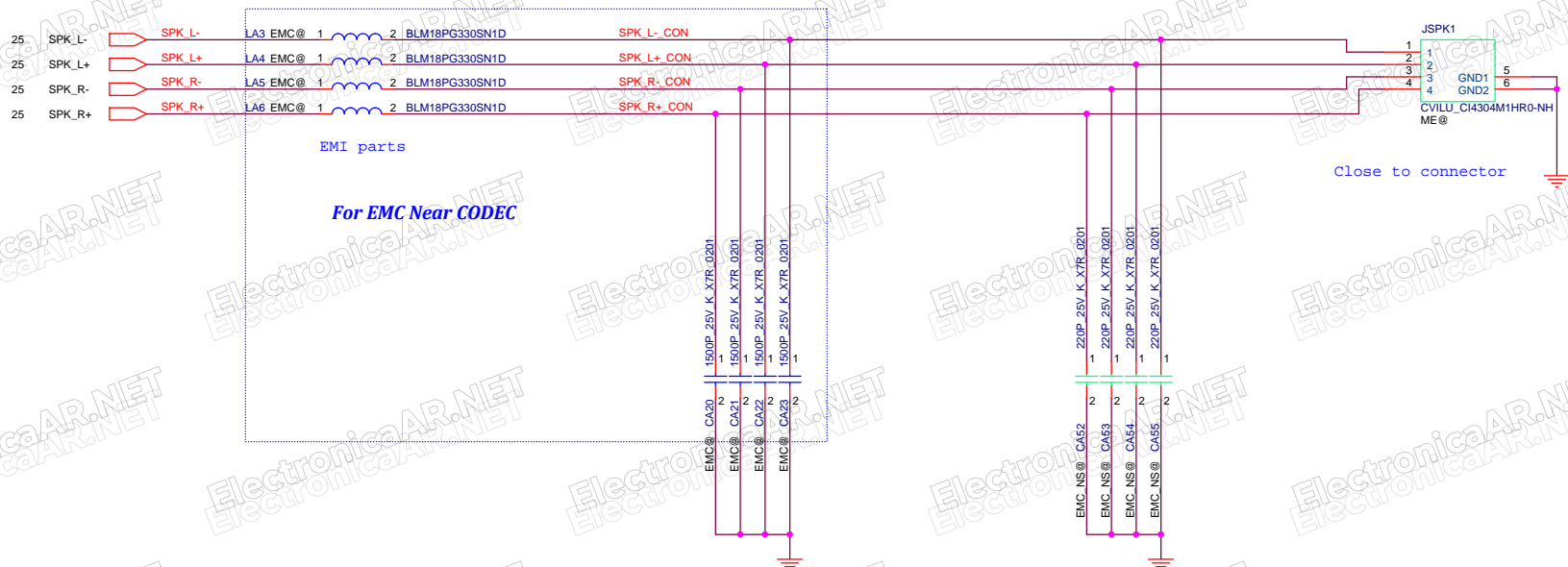
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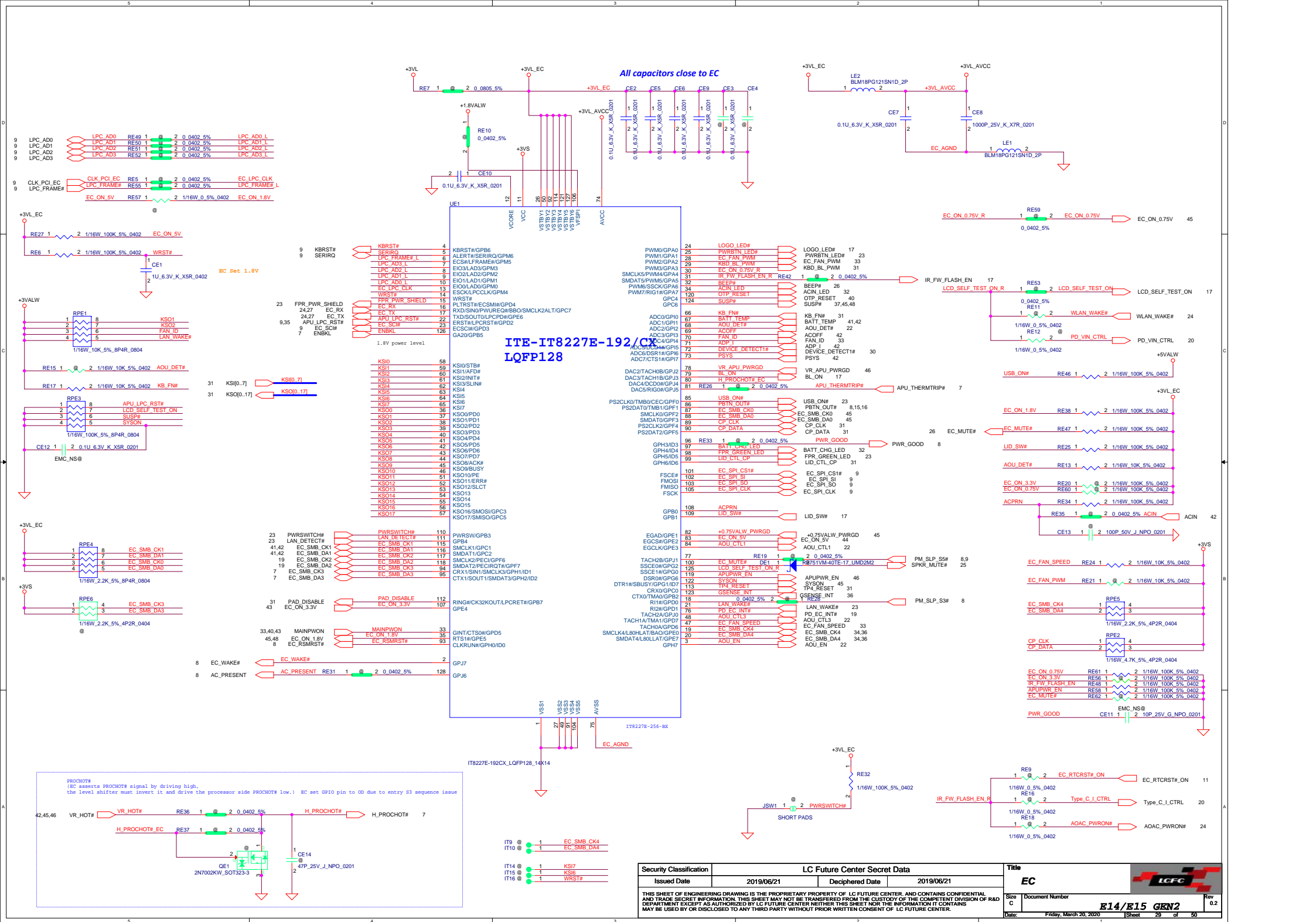


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Date:	Friday, March 20, 2020	Sheet	27	of	50

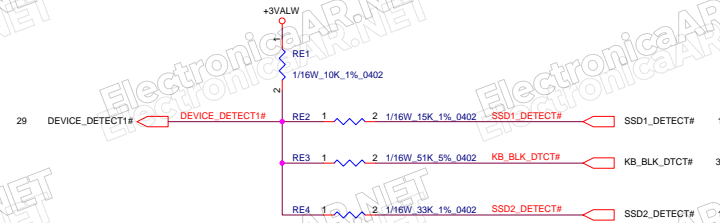
SPK CONN.



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CUSTOM						
Date:		Friday, March 20, 2020				
Sheet		28		of 50	Rev 0.2	

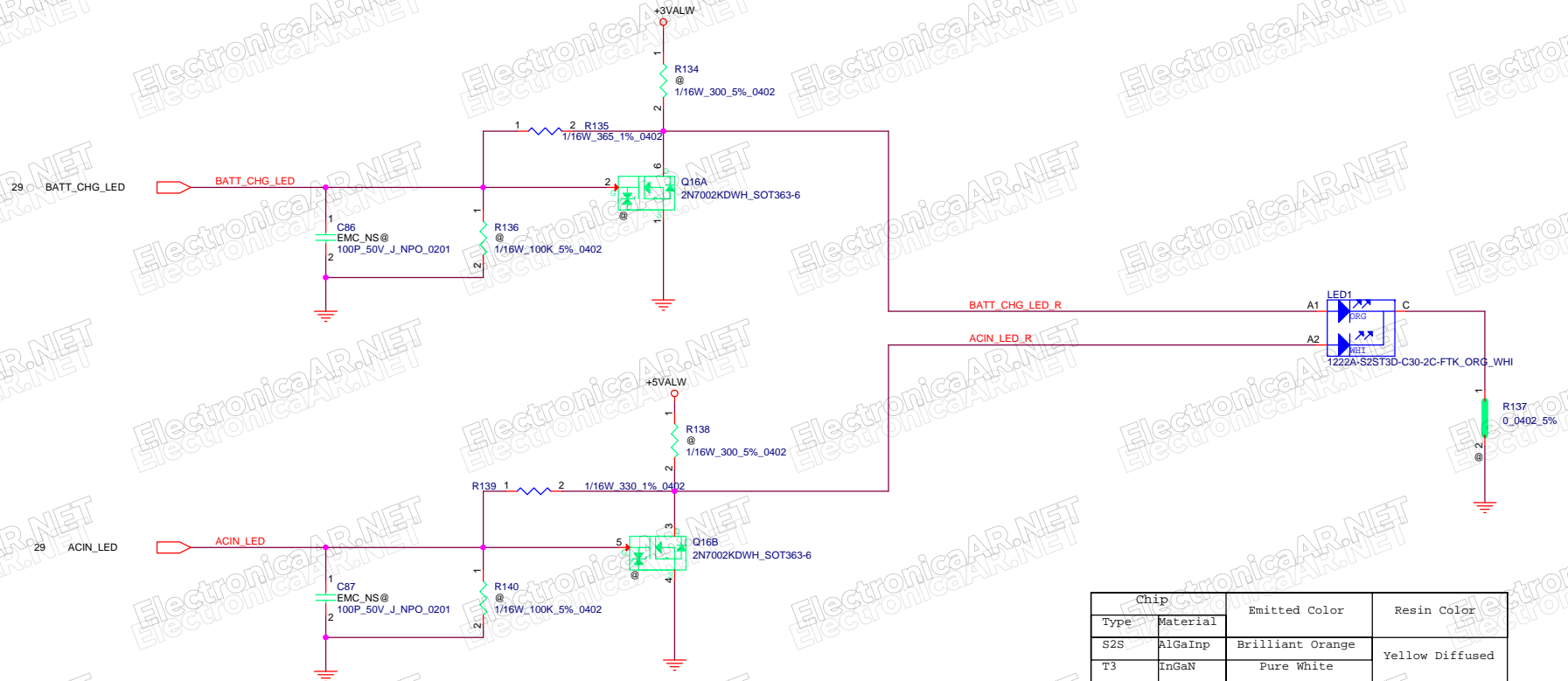


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Size	C	Document Number	E14/E15 GEN2	Rev 0.2
Date:	Friday, March 20, 2020	Sheet	29	of 50



Vcc	3.3V					
RE1	10K +/- 5%					
DEVICE_DETECT1#	1.98V	1.675V	1.772V	2.759V	2.533V	3.3V
SSD1_DETECT#	V	V	V	X	X	X
KB_BLK_DTCT#	X	V	X	X	V	X
SSD2_DETECT#	X	X	V	V	X	X

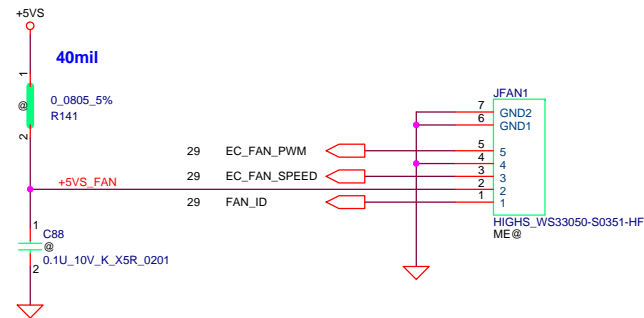
POWER ADAPTER Bi-COLOR(ORANGE/WHITE)



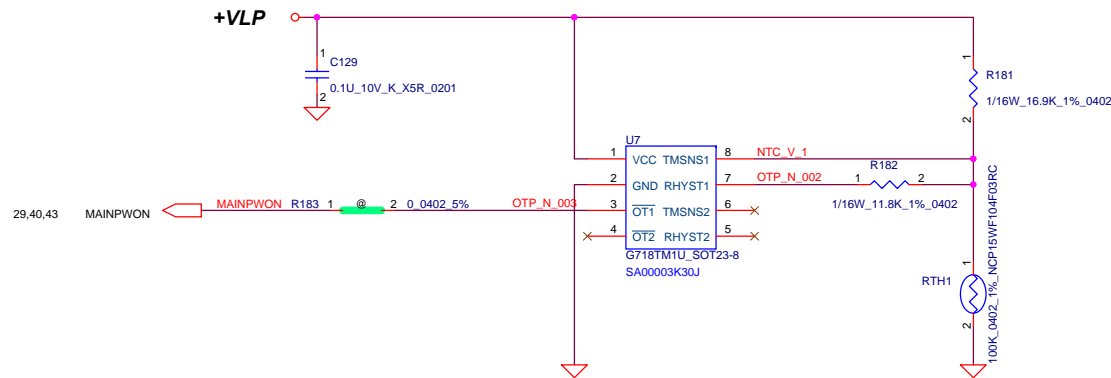
Chip		Emitted Color	Resin Color
Type	Material		
S2S	AlGaInp	Brilliant Orange	Yellow Diffused
T3	InGaN	Pure White	Yellow Diffused

Security Classification		LC Future Center Secret Data		Title	
Issued Date	2019/06/21	Deciphered Date	2019/06/21	Power LED	
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				E14/E15 GEN2	0.2
				Date: Friday, March 20, 2020	Sheet 32 of 50


FAN CONN.



HW Protect



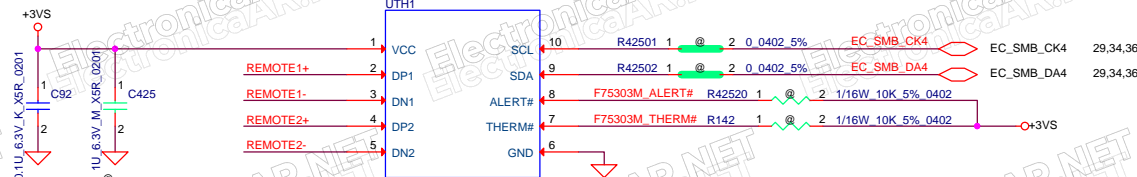
over temperature threshold:
 $RSET = 3 * RTMH$
 $100 \pm 30C$
Hysteresis temperature threshold.
 $RHYST = (RSET * RTML) / (3 * RTML - RSET)$
 $80 \pm 30C$

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Date:				Friday, March 20, 2020				Sheet		33 of 50	

- 1 Near
- 2 Remote

UTH1	
1	VCC
2	DP1
3	DN1
4	DP2
5	DN2

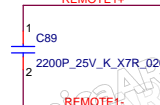
F75303M MSOP10



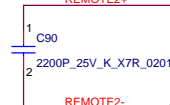
Address 1001 101xb

TABLE of Thermal Sensor (UTH1)		
Vendor	LCFC P/N	Description
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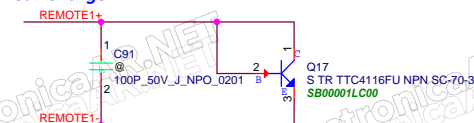
REMOTE1+



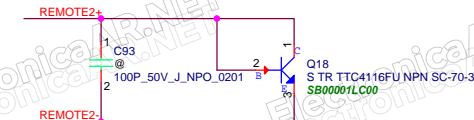
REMOTE2+



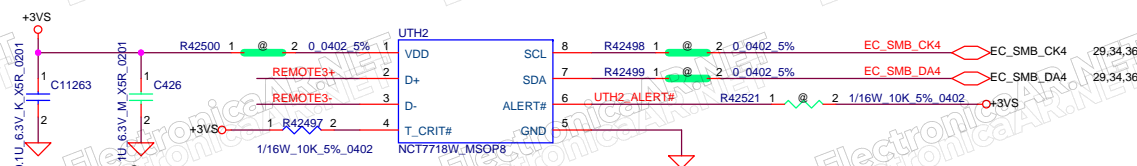
REMOTE14



SW 1111

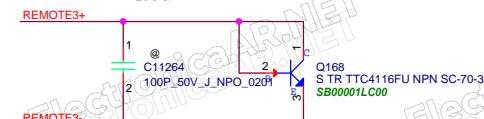


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REMOTE+/_R, REMOTE1+/_-, REMOTE2+/_-:  
Trace width/space:10/10 mil  
Trace length:<8"
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NCT7718W I2C/ SMBus™ address is 1001100xb (x is R/W bit).

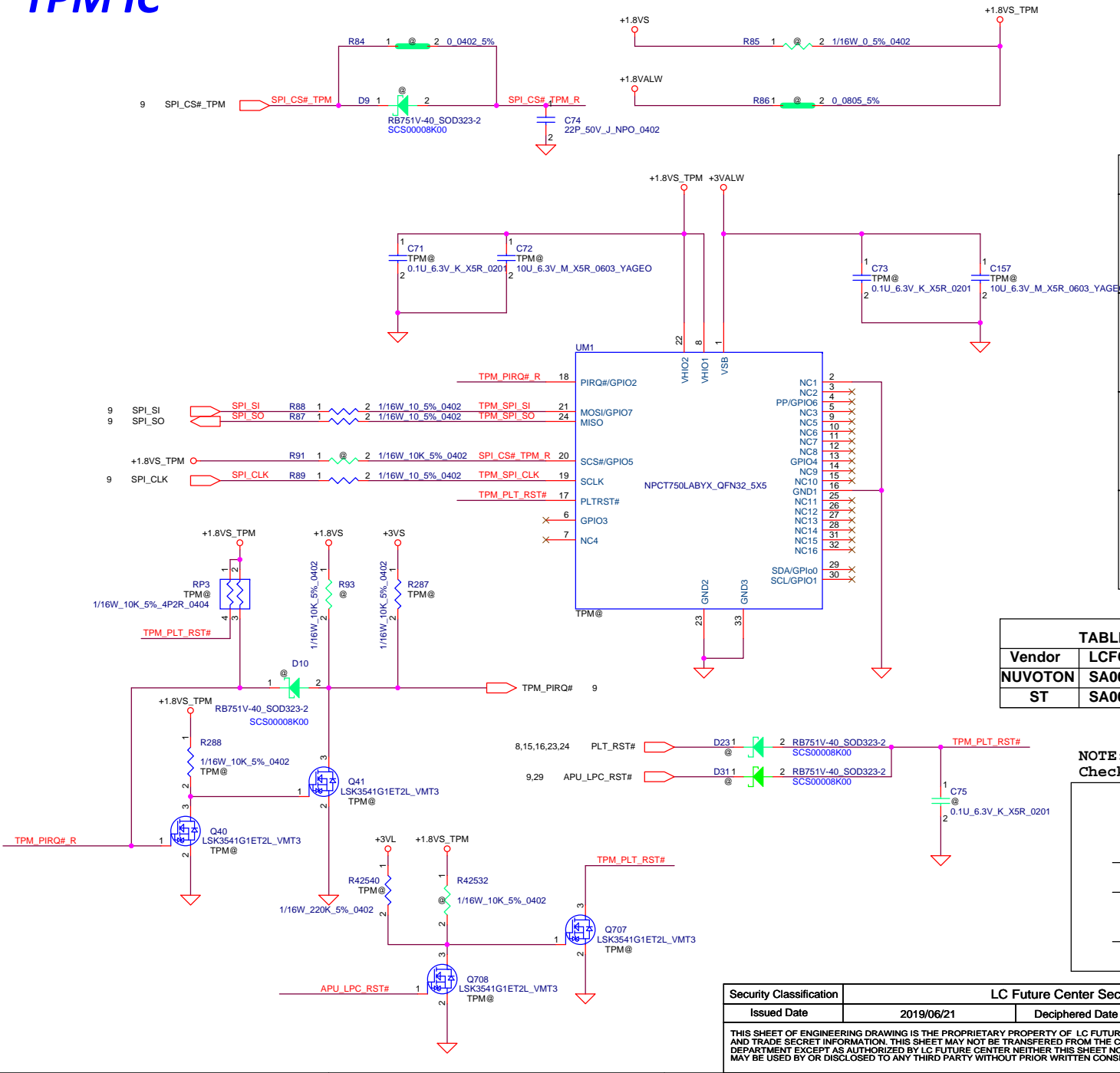
REMOTE3+



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Issued Date	2019/06/21	Deciphered Date	2019/06/21	Thermal Sensor		
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				Custom	E14/E15 GEN2	0.
				Date:	Friday, March 20, 2020	Sheet: 34 of 50

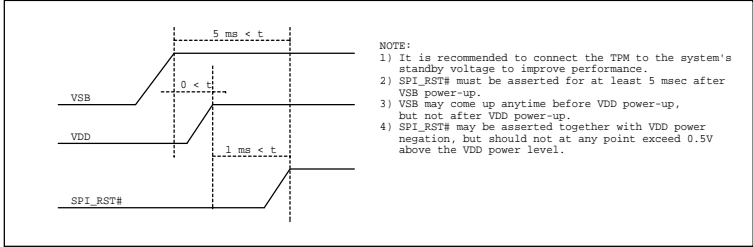
TPM IC



Pin	TCG PTP Spec (v 38)	Infineon SLB9670VQ1.2 FW6.10	ST Micro ST33HTPM2E32AAB9	Nuvoton NPC7650LB0YX
1	VDD	VDD	NC	VSB
2	GND	GND	NC	NC
3	GPIO	NC	NC	GPX/GPIO2
4	GPIO	NC	PP	PP
5	NC	NC	NC	TEST
6	VNC/GPIO	GPIO	NC	GPIO3
7	GPIO/VDD	PP	GPIO	NC
8	VDD	VDD	NC	VDD
9	GND	GND	NC	GND
10	VNC	NC	NC	NC
11	NC	NC	NC	NC
12	NC	NC	NC	Reserved
13	VNC/GPIO	NC	NC	GPIO4
14	VDD	NC	NC	VDD
15	NC	NC	NC	DMC
16	GND	NC	NC	GND
17	SPI_RST#	RST#	SPI_RST#	SPI_RST#
18	SPI_IRQ#	PIRQ#	SPI_IRQ#	SPI_IRQ#
19	SPI_CLK	SCLK	SPI_CLK	SCLK
20	SPI_CS#	CS#	SPI_CS#	SCS#
21	MOSI	MOSI	MOSI	MOSI
22	VDD	VDD	VDD	VDD
23	GND	GND	NC	GND
24	MISO	MISO	MISO	MISO
25	NC	NC	NC	NC
26	NC	NC	NC	NC
27	NC	NC	NC	(SERIRQ)
28	NC	NC	NC	DMC
29	VNC/GPIO	NC	NC	GPIO0
30	VNC/GPIO	NC	NC	GPIO1
31	VNC	NC	NC	NC
32	GND	GND	NC	GND

TABLE of TPM (UTPM1)		
Vendor	LCFC P/N	Description
NUVOTON	SA00008KS20	S IC NPCT750LABYX QFN 32P TPM 2.0 QS/MP
ST	SA000089E20	S IC ST33HTPH2E32AHC0 VQFN 32P TPM 2.0

NOTE:
Check timing sequence in SDV phase.



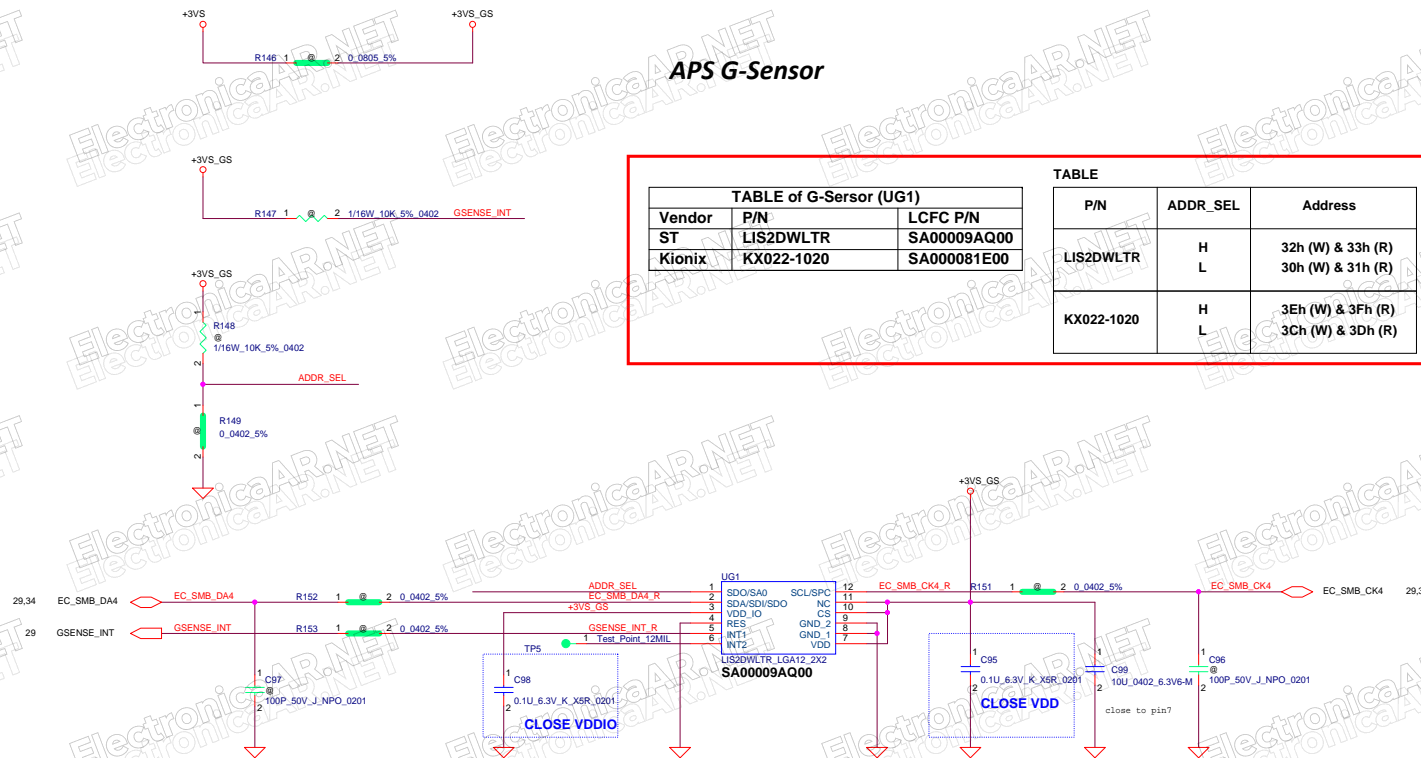
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				Date:	Friday, March 20, 2020	Sheet	35 of 50	

APS G-Sensor

Vendor	P/N	LCFC P/N
ST	LIS2DWLTR	SA00009AQ00
Kionix	KX022-1020	SA000081E00

TABLE

P/N	ADDR_SEL	Address
LIS2DWLTR	H L	32h (W) & 33h (R) 30h (W) & 31h (R)
KX022-1020	H L	3Eh (W) & 3Fh (R) 3Ch (W) & 3Dh (R)



+3VS load Switch

+3VALW to +3VS 6.2A request
V9B+ 9V--13V

+5VS load Switch

+5VALW to +5VS 2.6A request

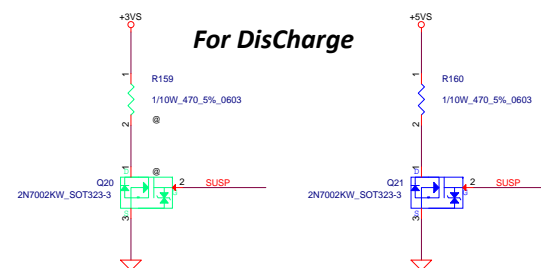
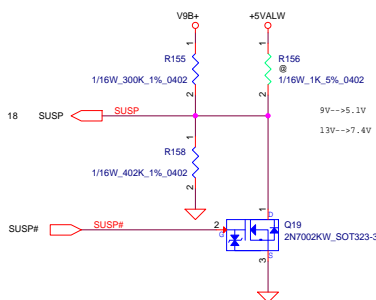
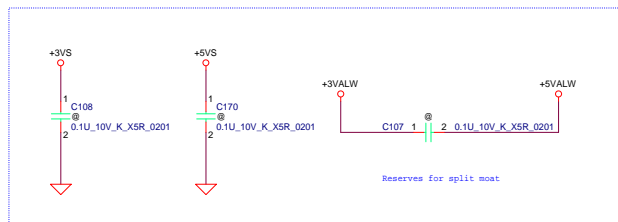
Load MOS N MOS Id =< 40A Vgs(th) Max >= 2.6V
Rds(on) >= 7.5mohm

+/- 1.5%

Load MOS N MOS Id =< 40A Vgs(th) Max >= 2.6V
Rds(on) >= 7.5mohm

+/- 1.5%

For DisCharge



+VDDP load Switch

+0.75VALW_VDDP to +0.75VDDP 10A request
V9B+ 9V--13V

+1.8VS load Switch

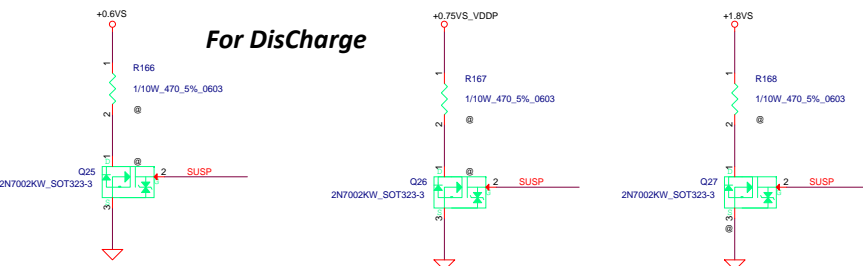
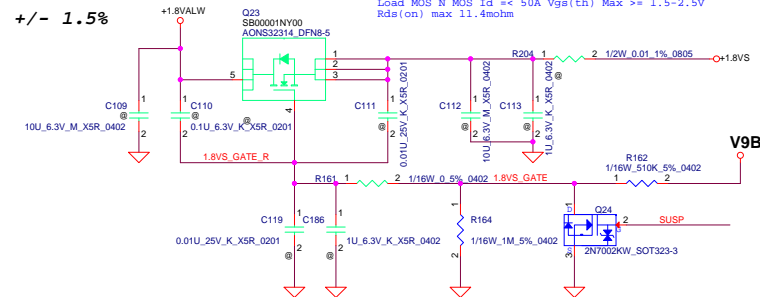
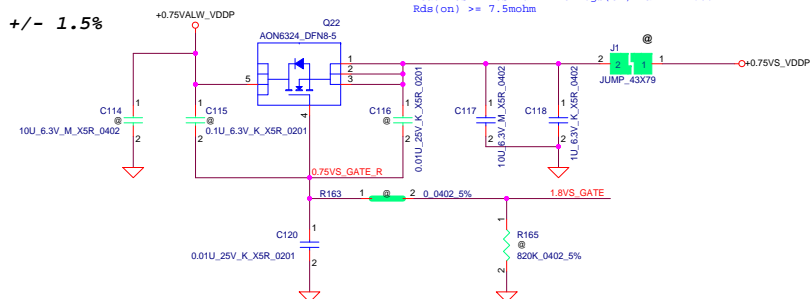
+1.8VALW to +1.8VS 2A request


Load MOS N MOS Id =< 50A Vgs(th) Max >= 1.5-2.5V
Rds(on) max 11.4mohm

+/- 1.5%

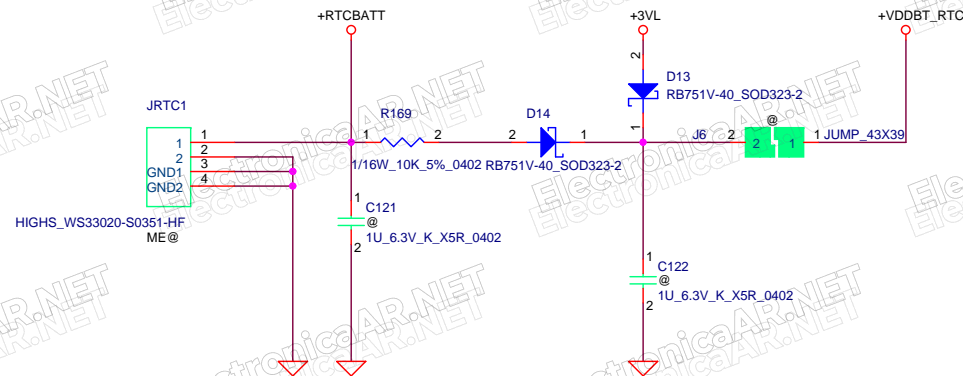
+/- 1.5%

For DisCharge




Security Classification	LC Future Center Secret Data			Title	3VS/5VS/0.9VS/1.8VS	
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Size C		Document Number		E14/E15 GEN2		Rev 0
Date		Friday, March 20, 2020		Sheet 37 of 50		

RTC BATTERY

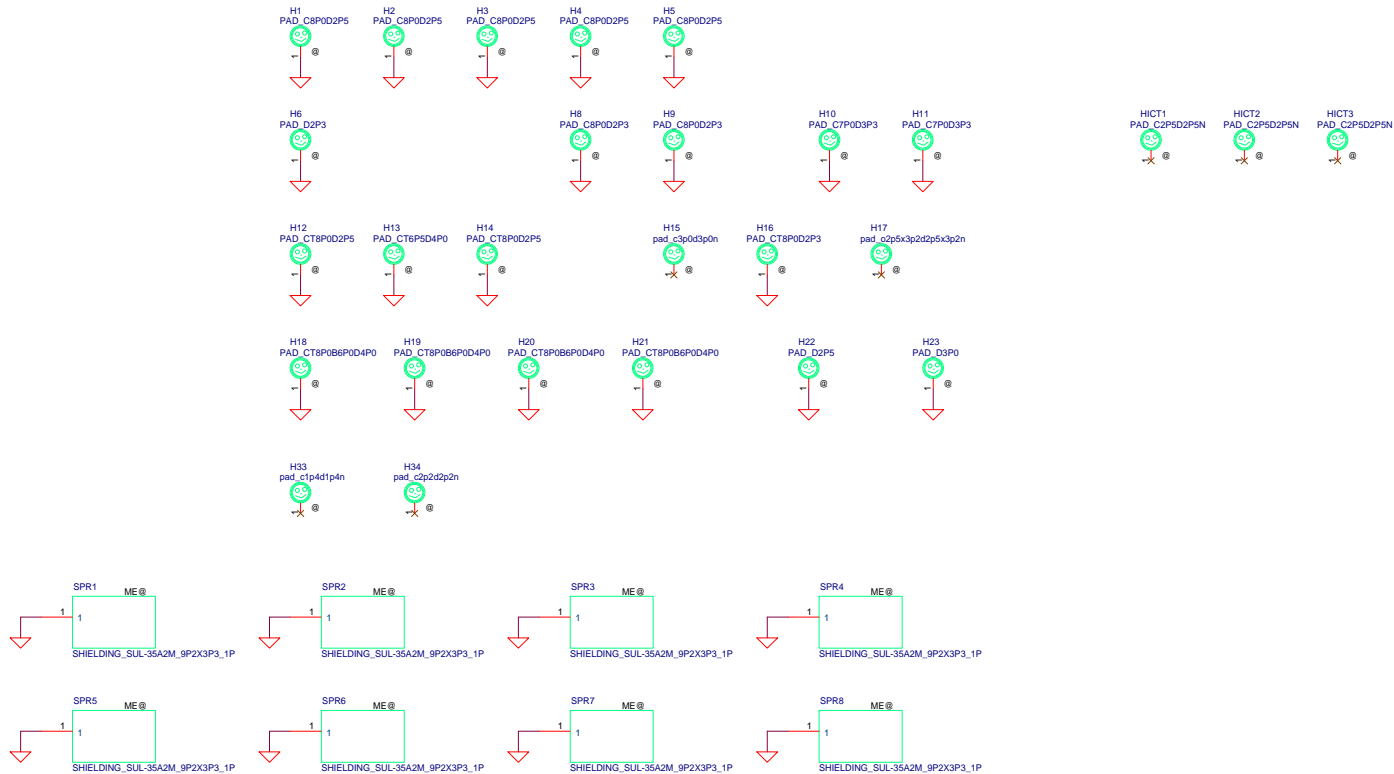


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Issued Date	2019/06/21	Deciphered Date	2019/06/21	RTC Battery	
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				E14/E15 GEN2	
Date:		Friday, March 20, 2020		Sheet	38 of 50




Rev 0.2

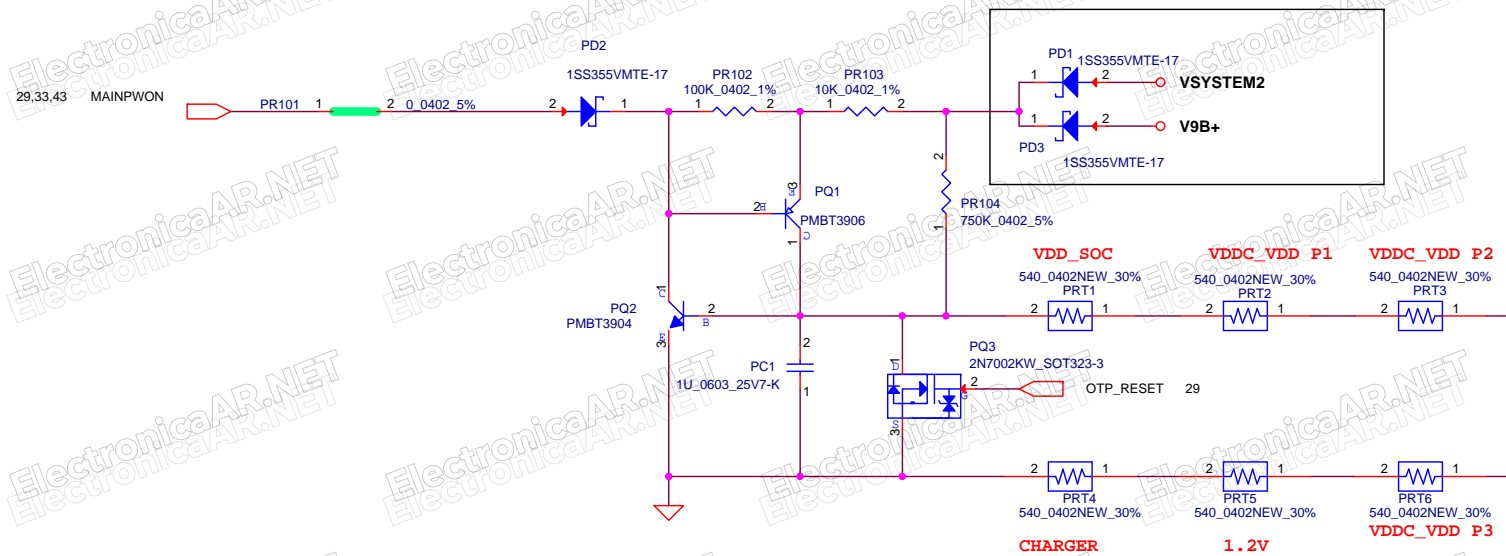
Screw Hole



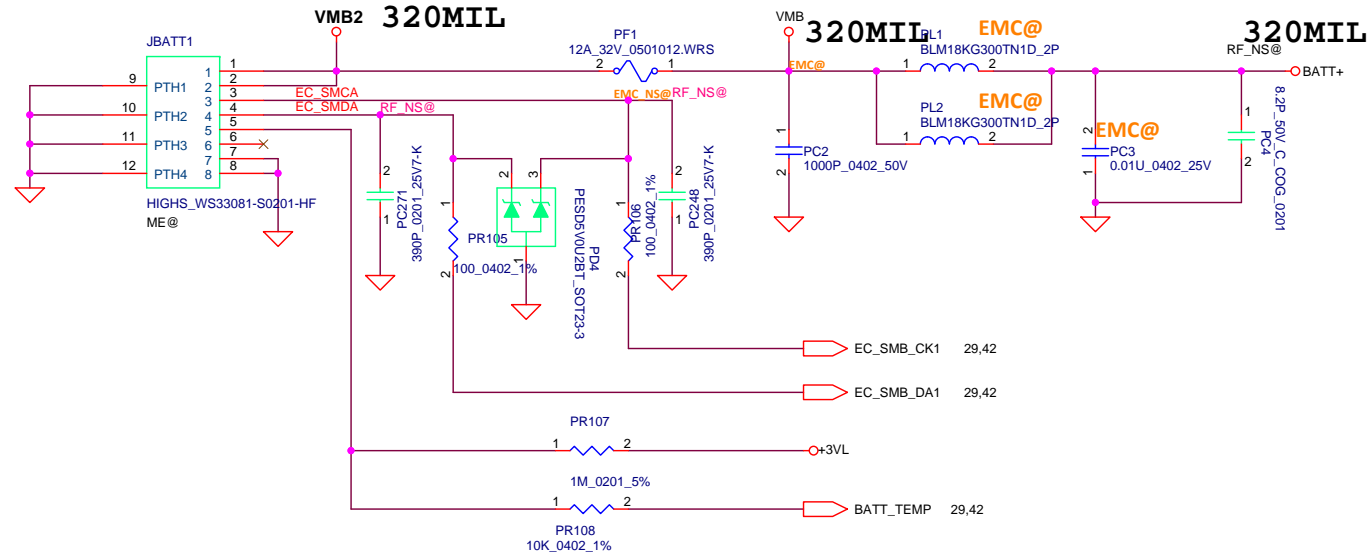
PCB Federal Mark PAD



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Security Classification		LC Future Center Secret Data		Title	
Issued Date	2018/03/26	Deciphered Date	2019/06/01	DCIN / VIN Detector	
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					E14/E15
				Date:	Friday, March 20, 2020
				Sheet	40 of 50

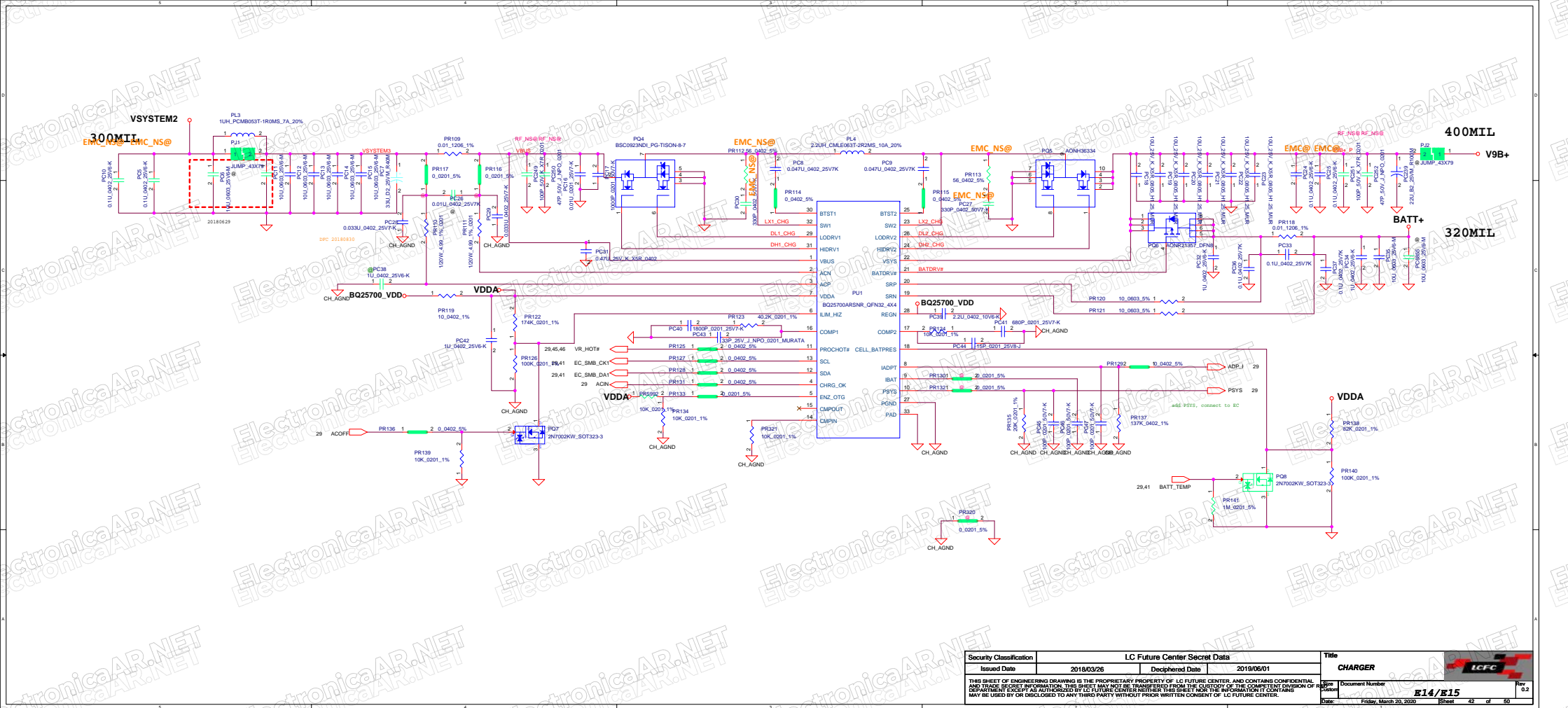


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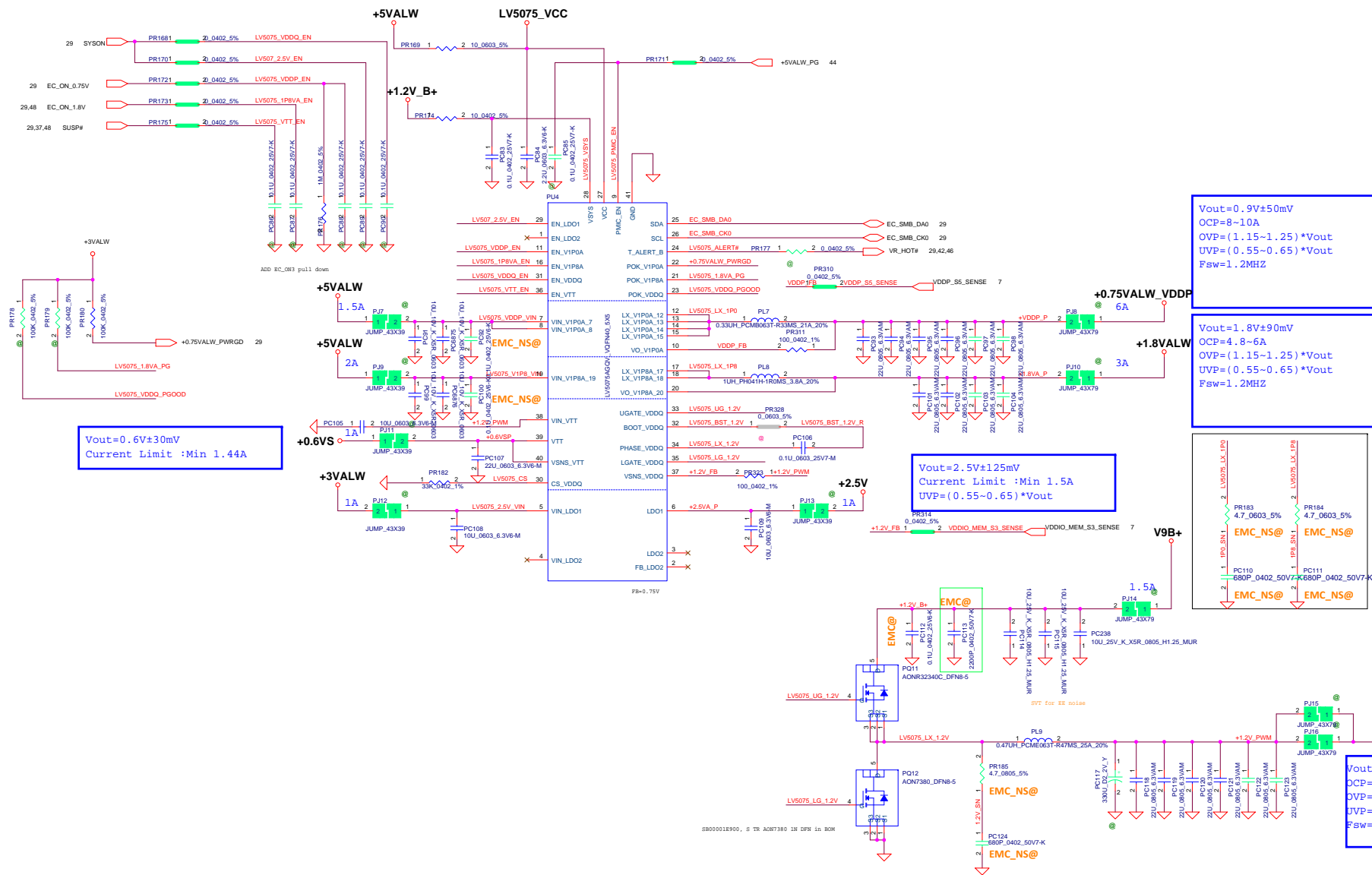
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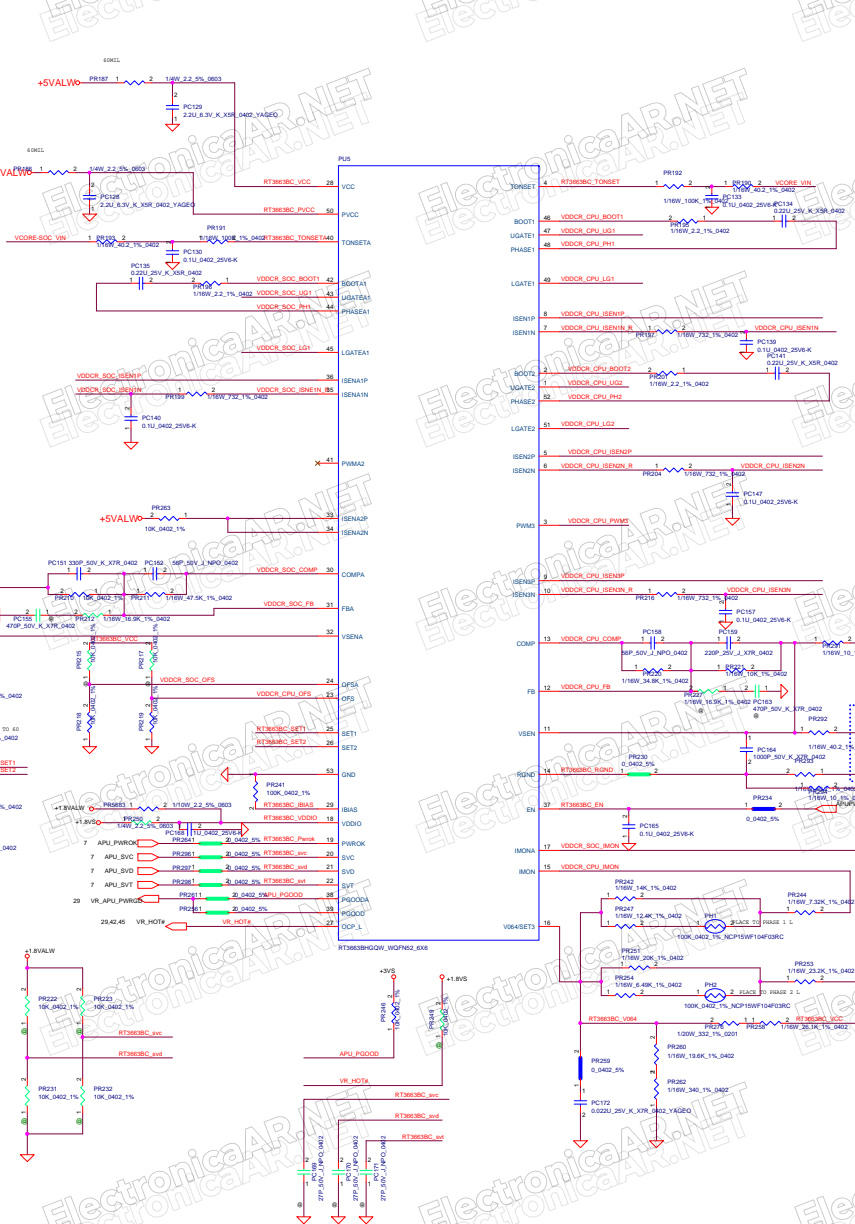
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Size	Document Number	E14/E15	
Date:	Friday, March 20, 2020	Sheet	41 of 50



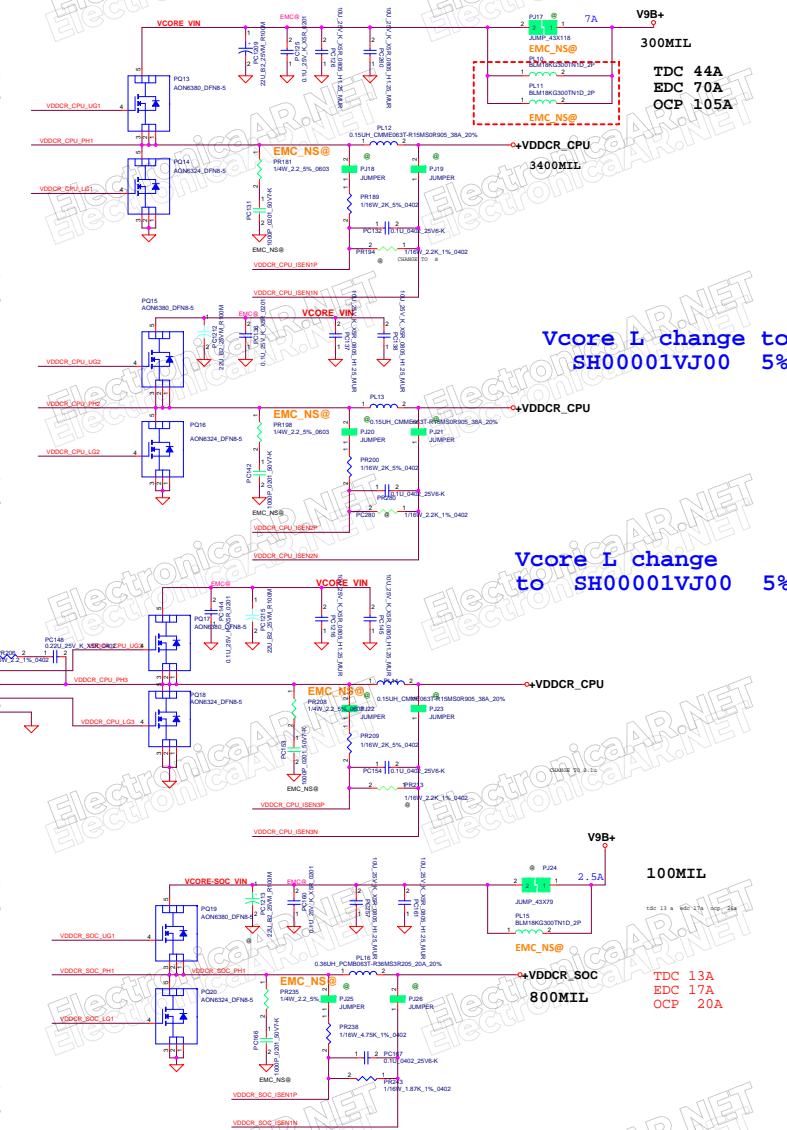


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Document Number		E14/E15		Rev	
Date		Friday, March 20, 2020		0.2	





PRE-PWROK METAL VID CODES		
SVC	SVD	Boot Voltage
0	0	1.1V
0	1	1.0V
1	0	0.9V(Default)
1	1	0.8V



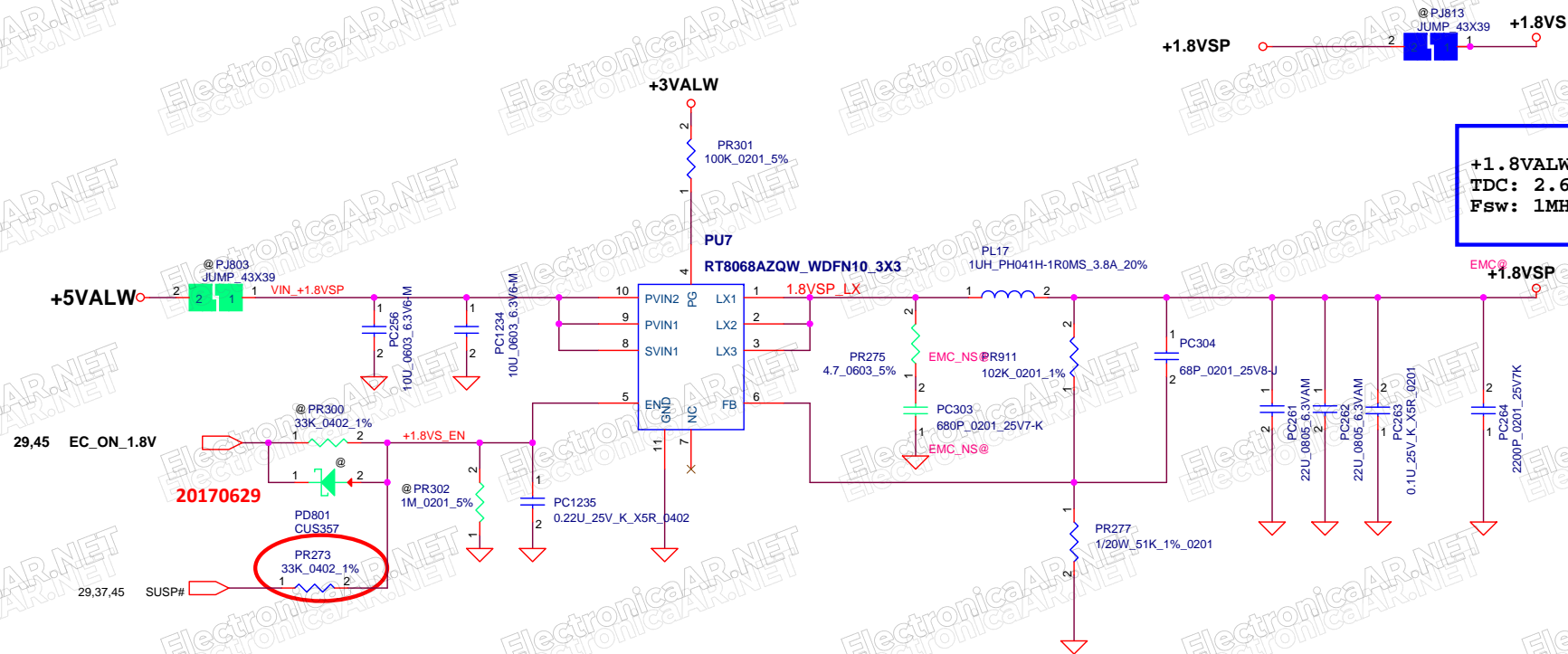
Vcore L change to SH00001VJ00 5%

Vcore L change to SH00001VJ00 5%

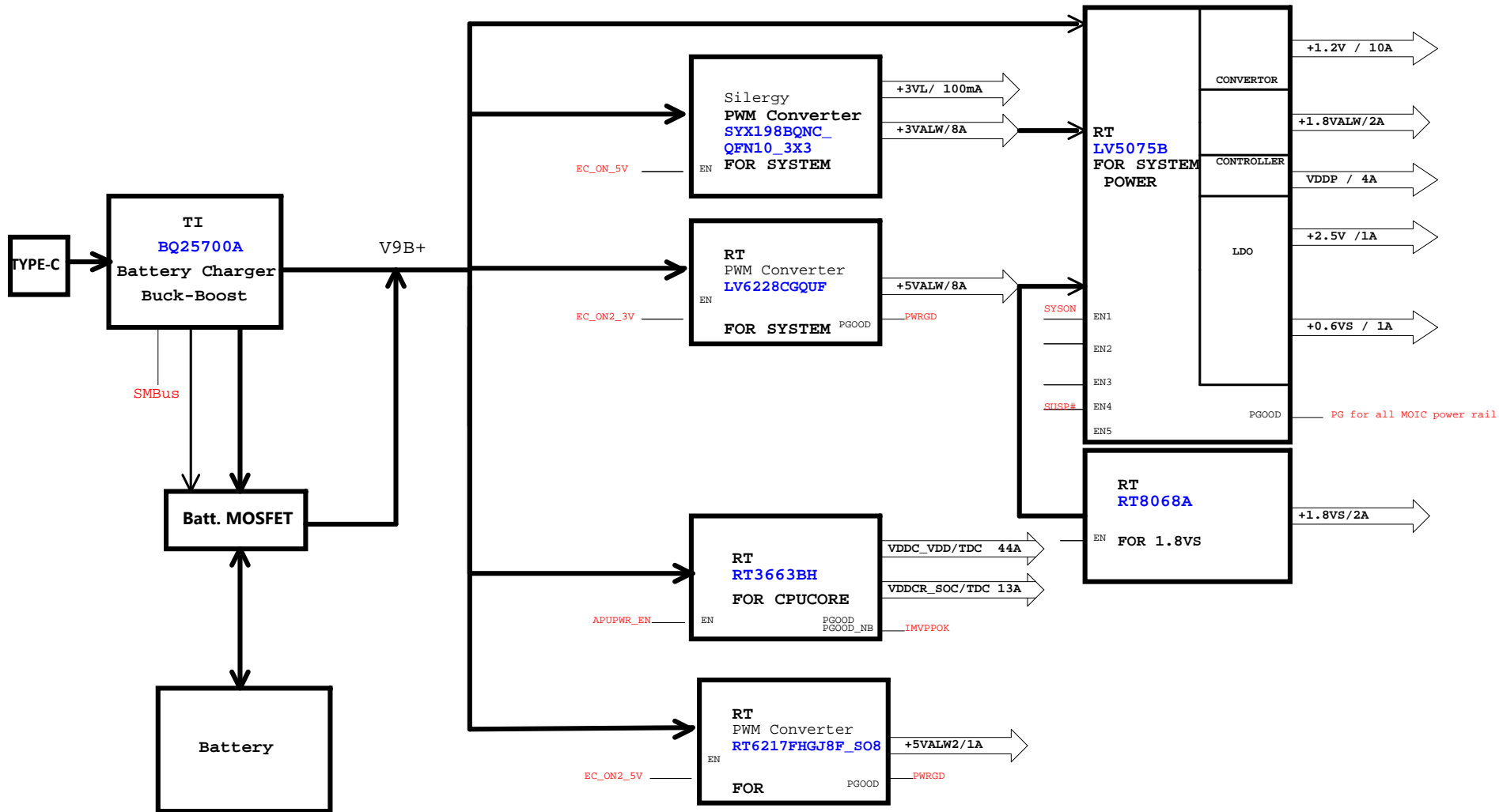
100MIL

TDC 13A
EDC 17A
OCP 20A


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					E14/E15
				Date:	Friday, March 20, 2020
				Sheet	48 of 50
				Rev	0.2



<Variant Name>

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Size		Document Number		E14/E15		Rev 0.2	
Date:		Friday, March 20, 2020		Sheet 49 of 50			

- 1,change 5V back to LV6228CGQUF
- 2,add 0.75V and 1.2V remote sense and device sense
- 3,change the charger follow T14
- 4,change the 1.8V input power to 5Valws
- 5,add 5Von off page
- 6,add +3V_PWRGD offpage
- 7,change 0.75V /1.8Voutput cap0603 to 0805
- 8,change Vcore and Vcore SOC input cap to 0805
- 9.change the charger output cap to 0805
- 10,change the 1.2V output cap
- 11,change the input cap 0805 to to 0805 of SE0000QK2D
- 12,change netname 5Valw 2 to +5Valws_2


20190702

- 1,chang the +5VALW2_Mode to the mode pin
- 2,change input cap to SE00000QKMT
- 3,delete APU power ok pull high of power side

2019.7.8

- 1,change Vcore highside mofet to AON6380
- 2,chaneg 1.2V to AONR32340C
- 3,delete some Vcore input cap and output cap follow S540

<Variant Name>

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